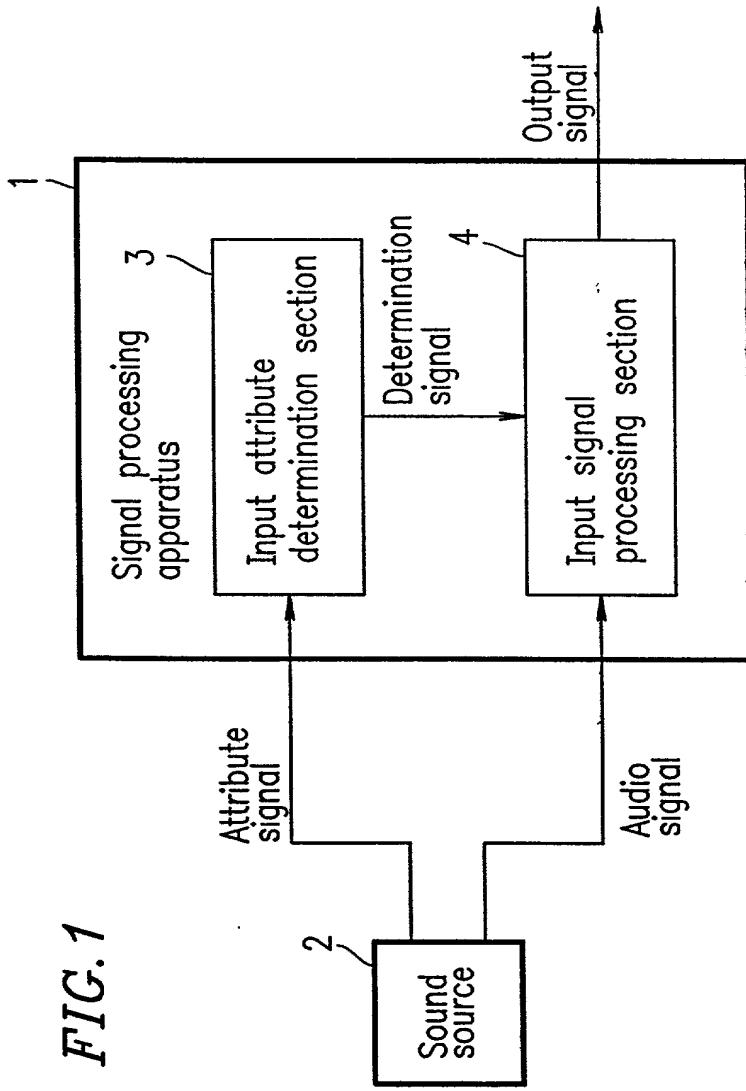


FIG. 1



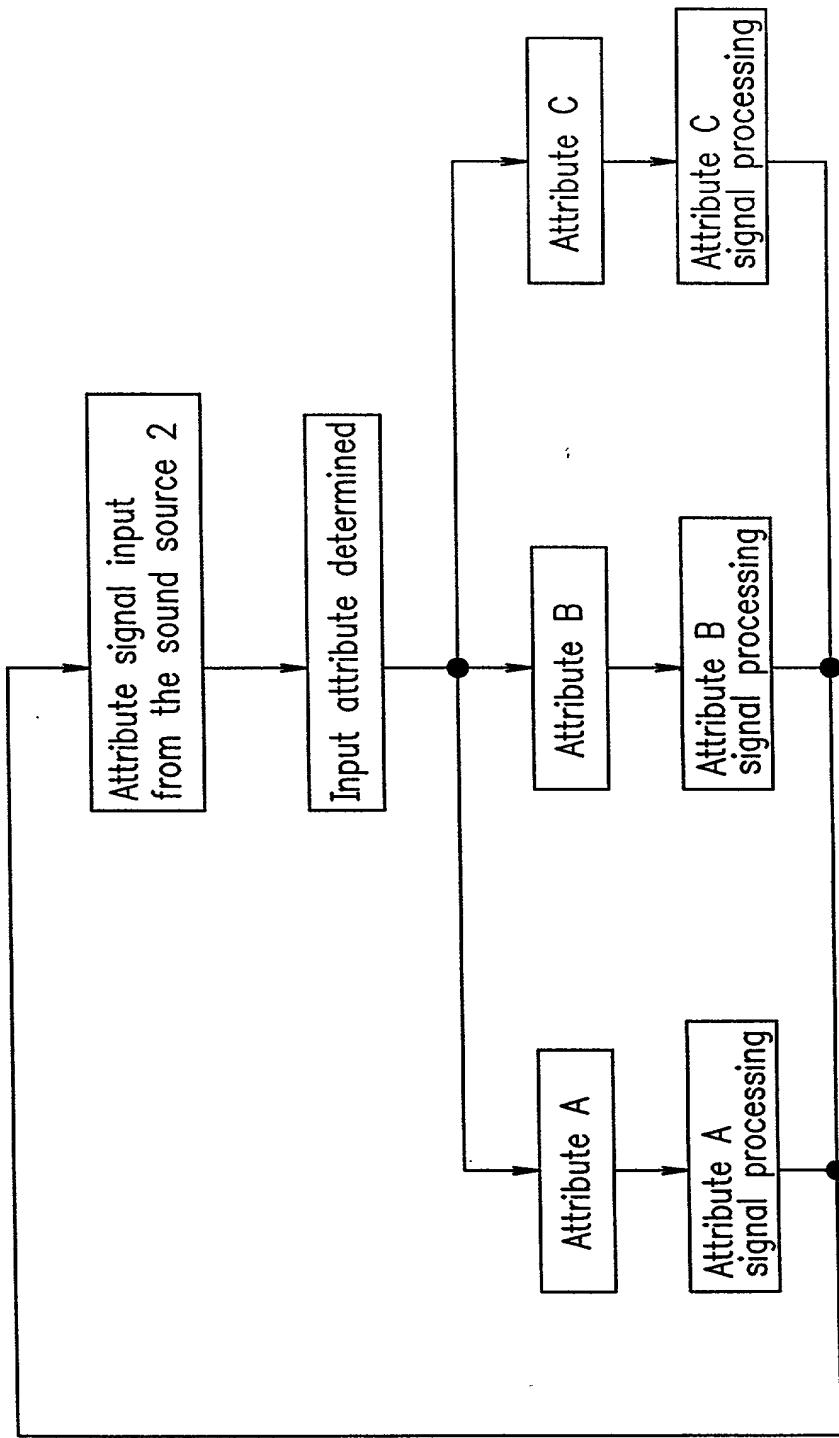
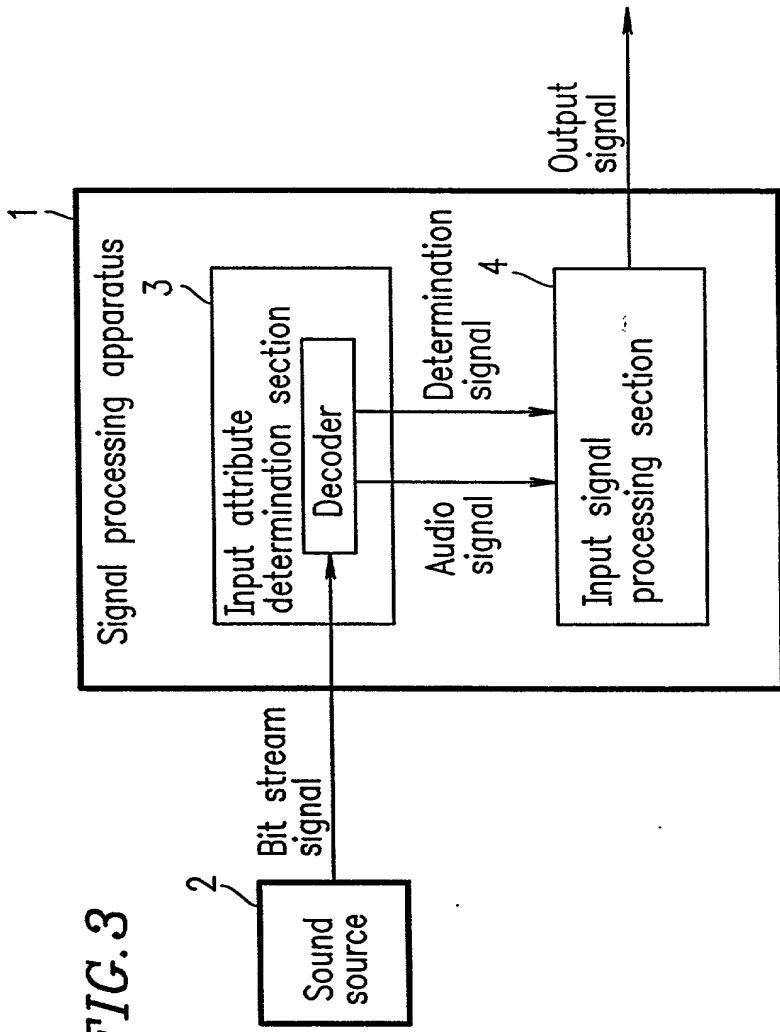
*FIG. 2*

FIG. 3  
Signal processing apparatus



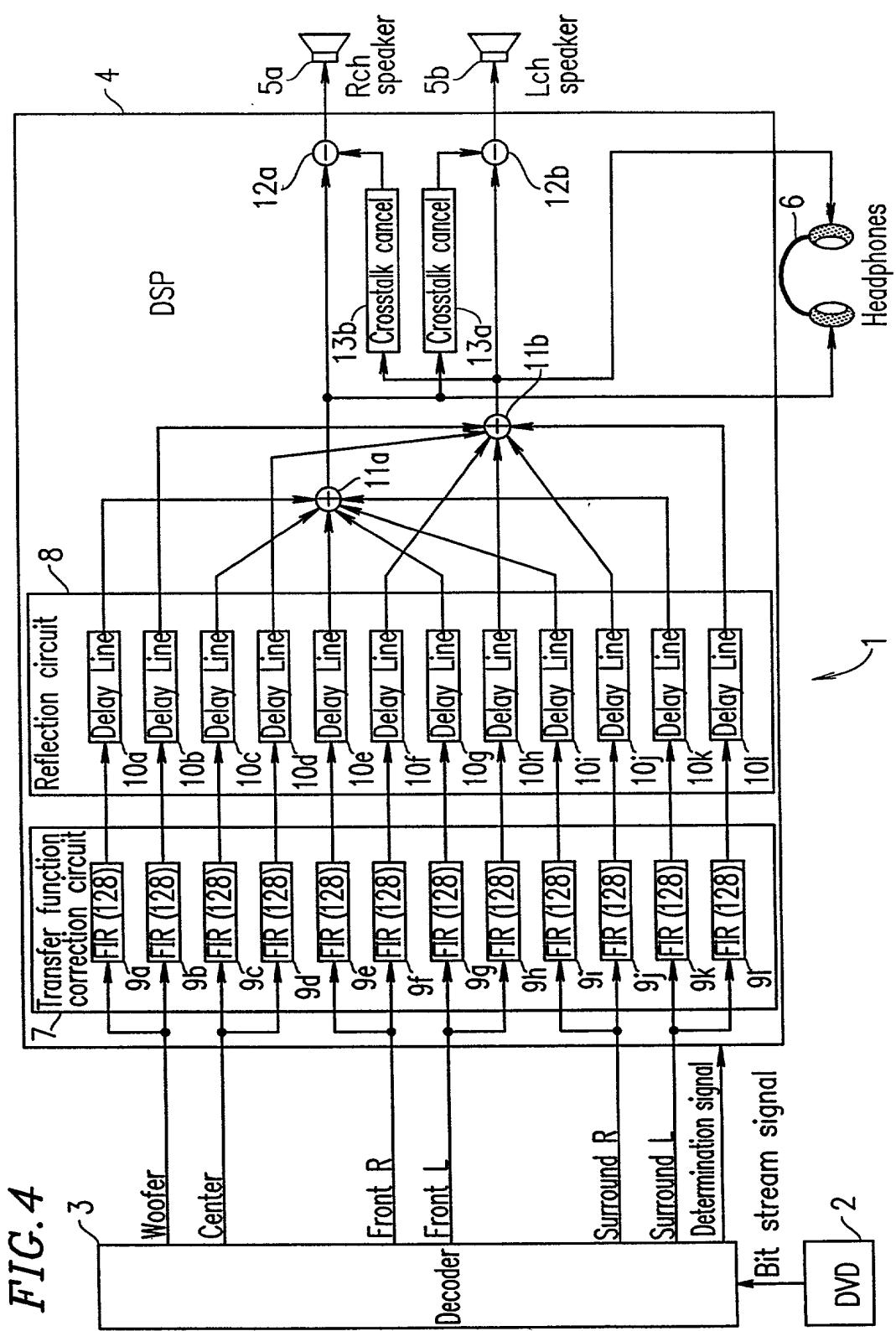


FIG. 5

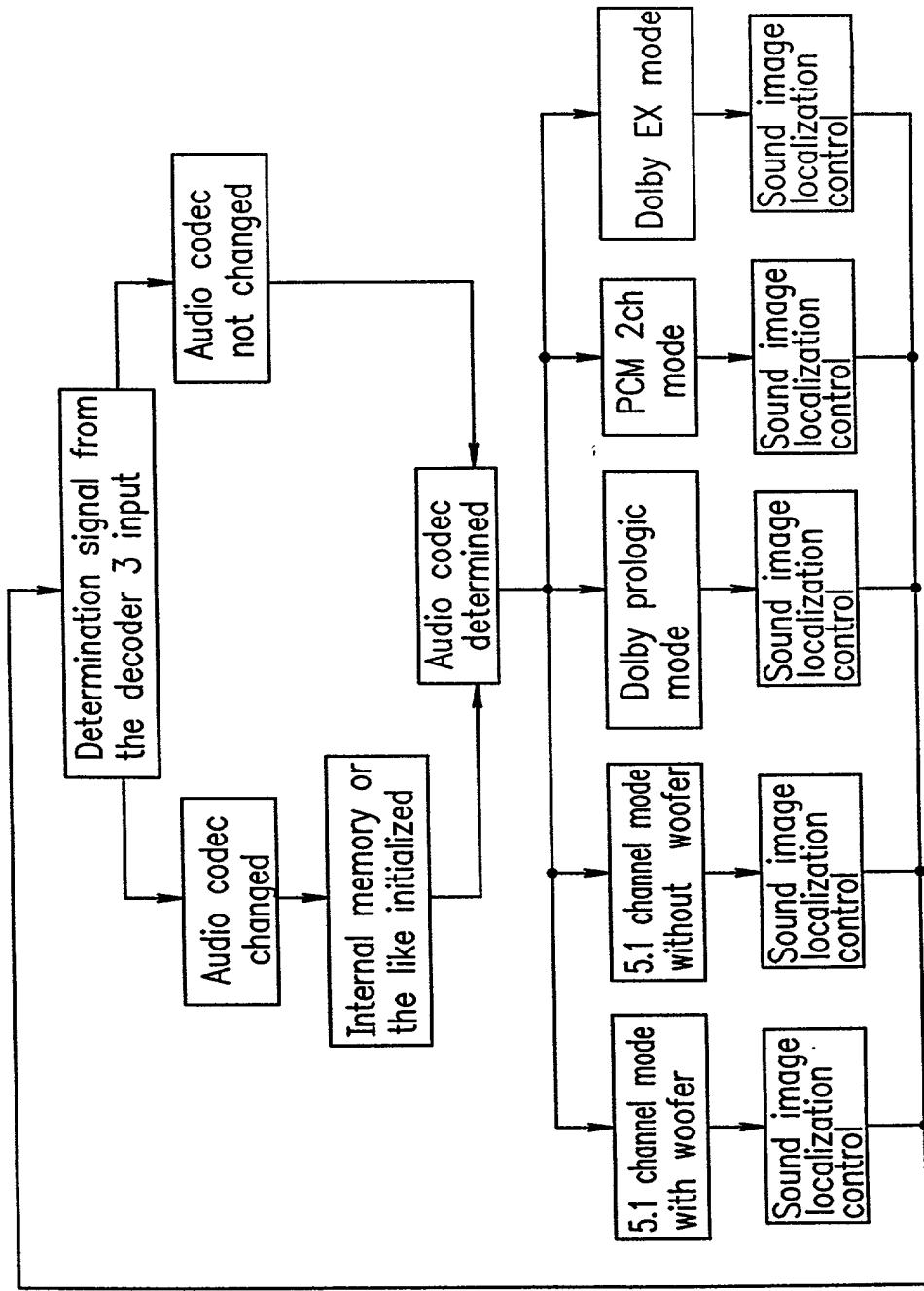


FIG. 6

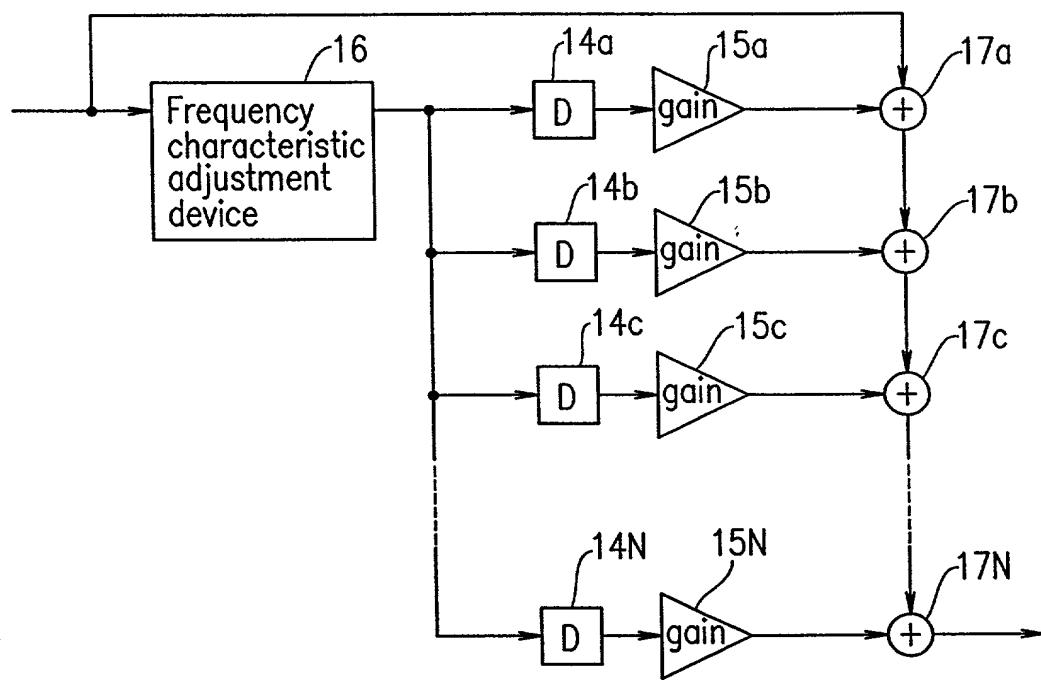


FIG. 7

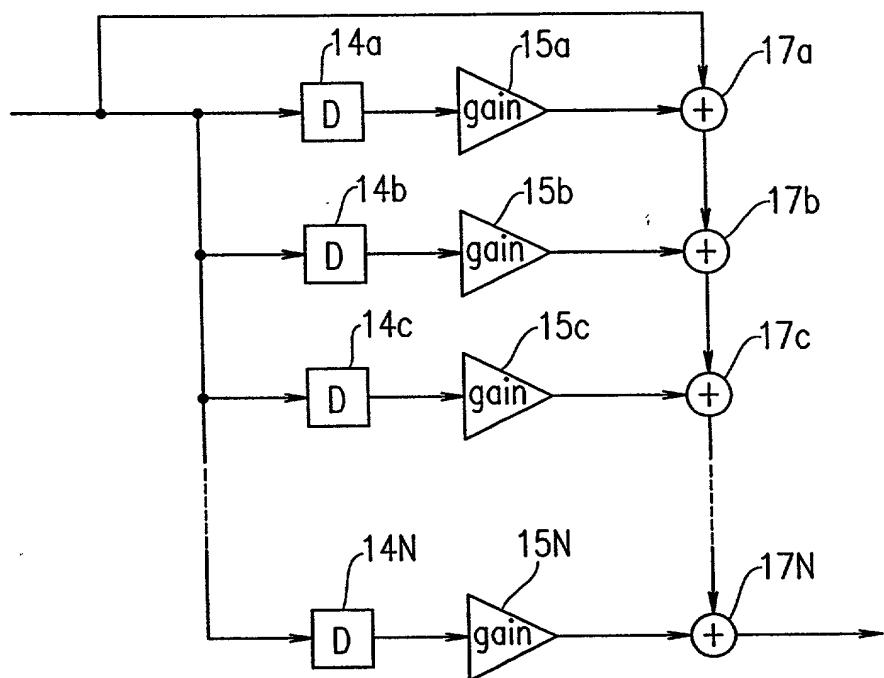


FIG. 8

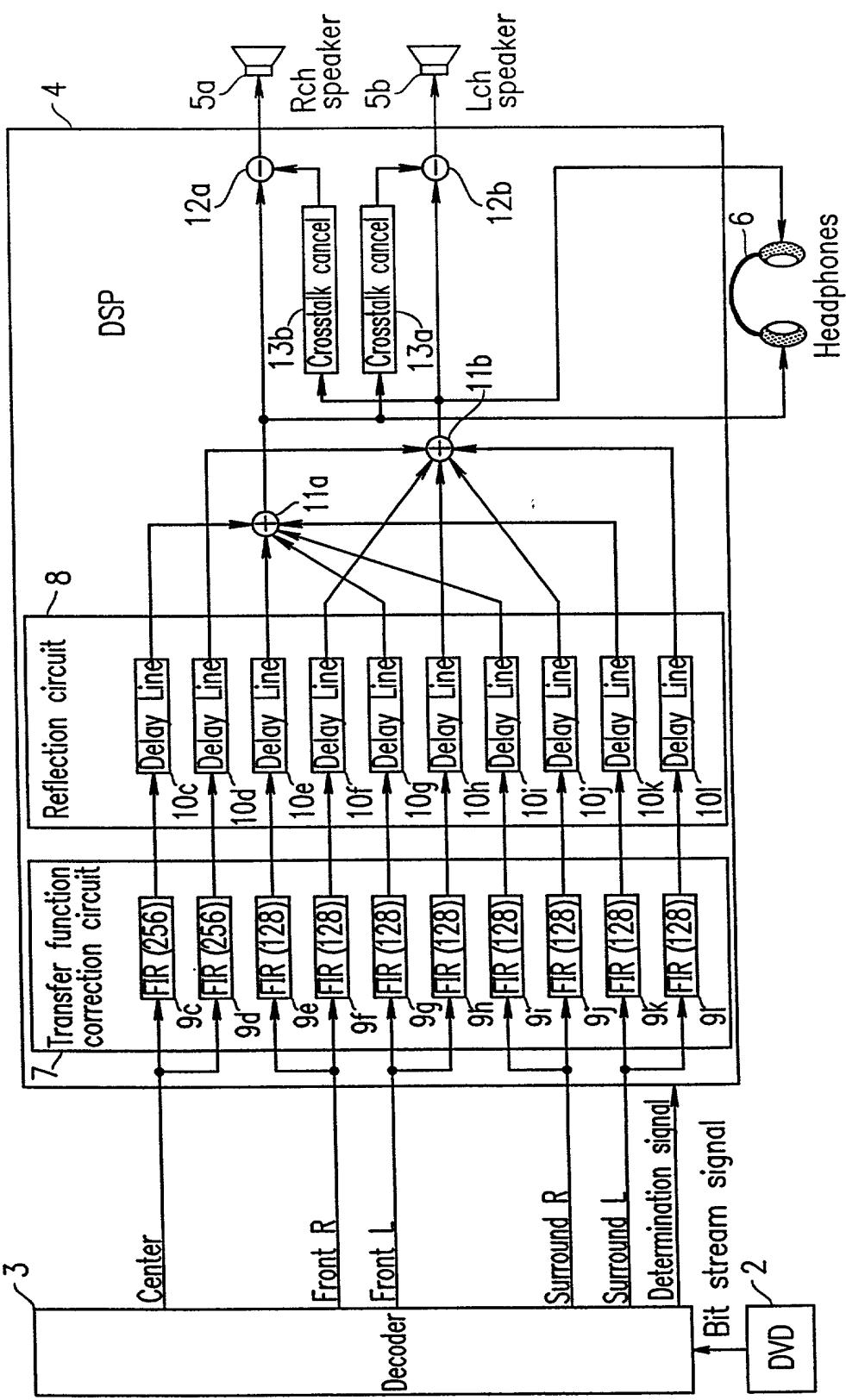


FIG. 9

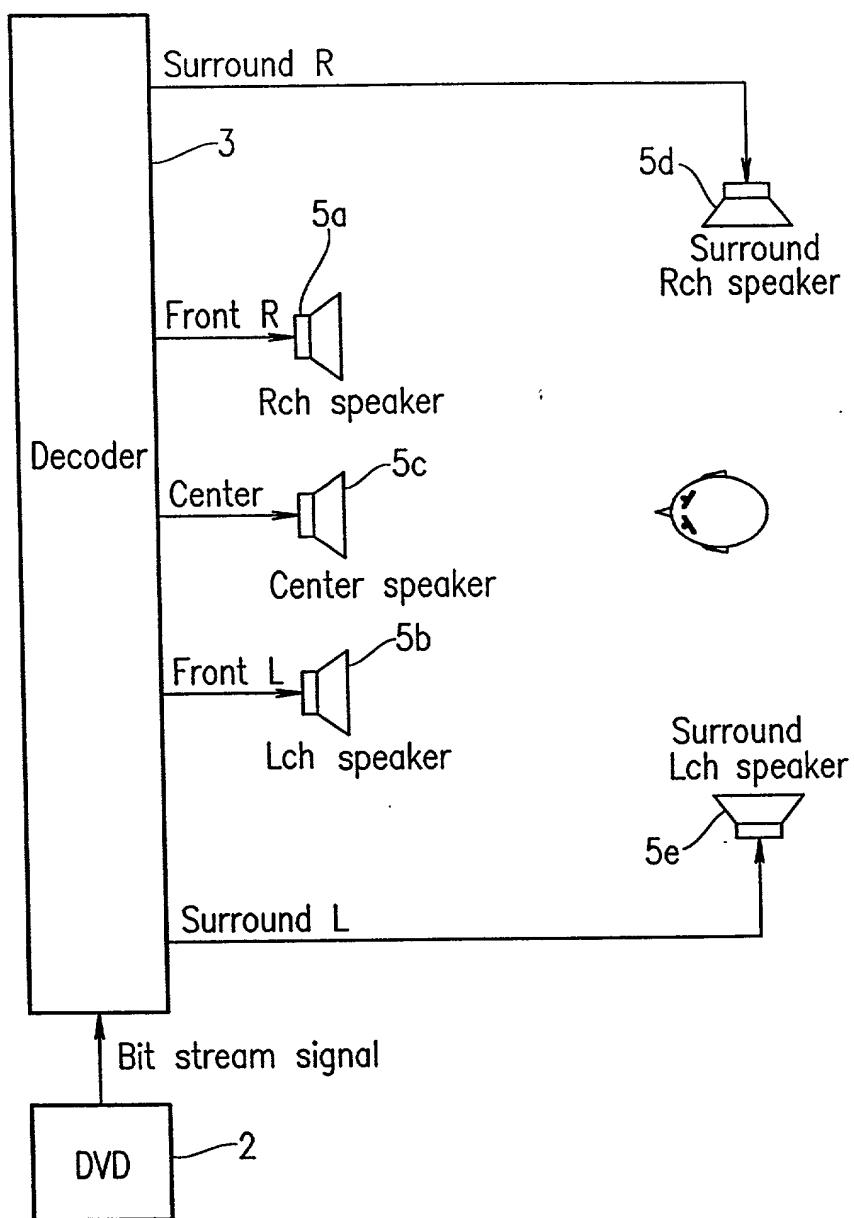
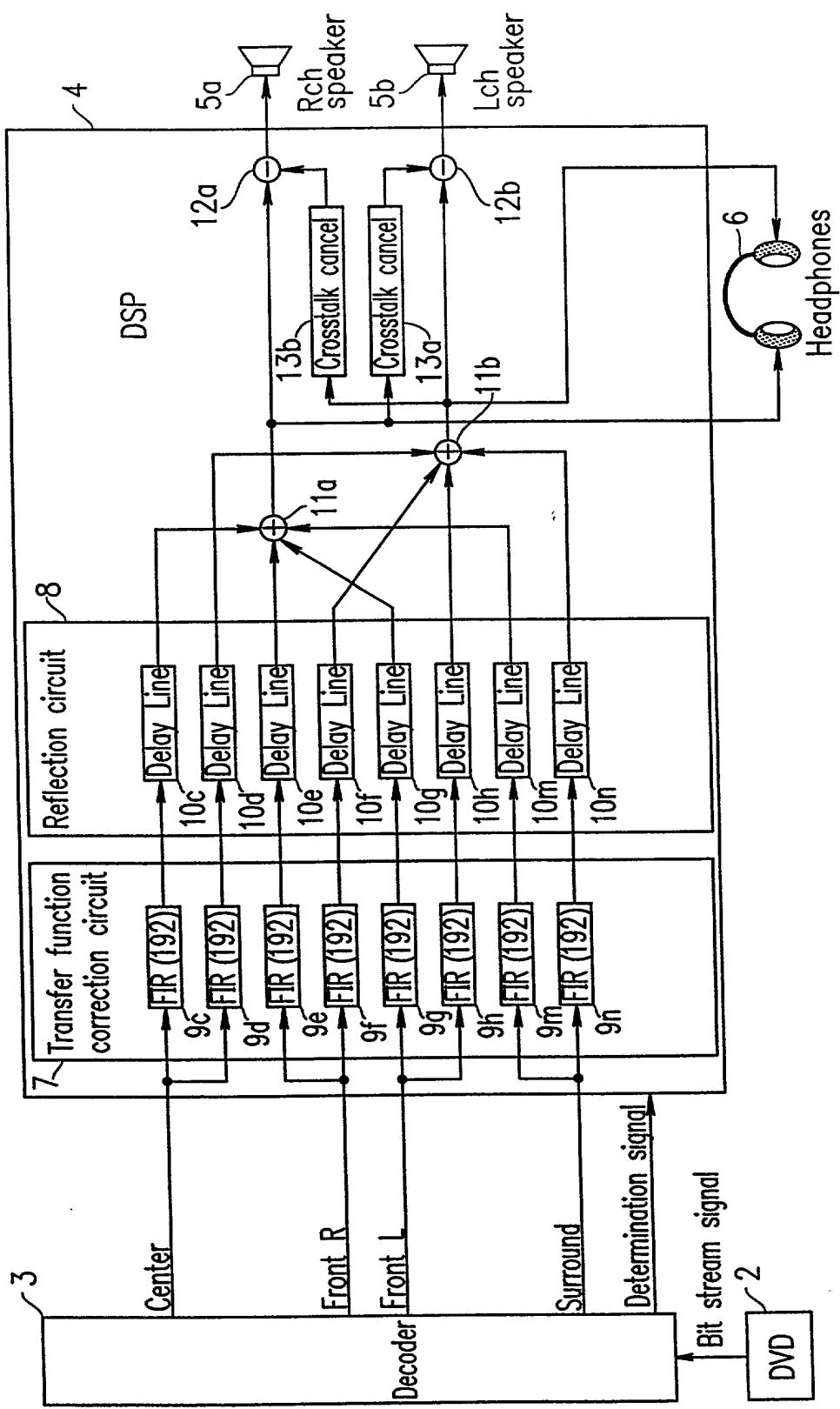


FIG. 10



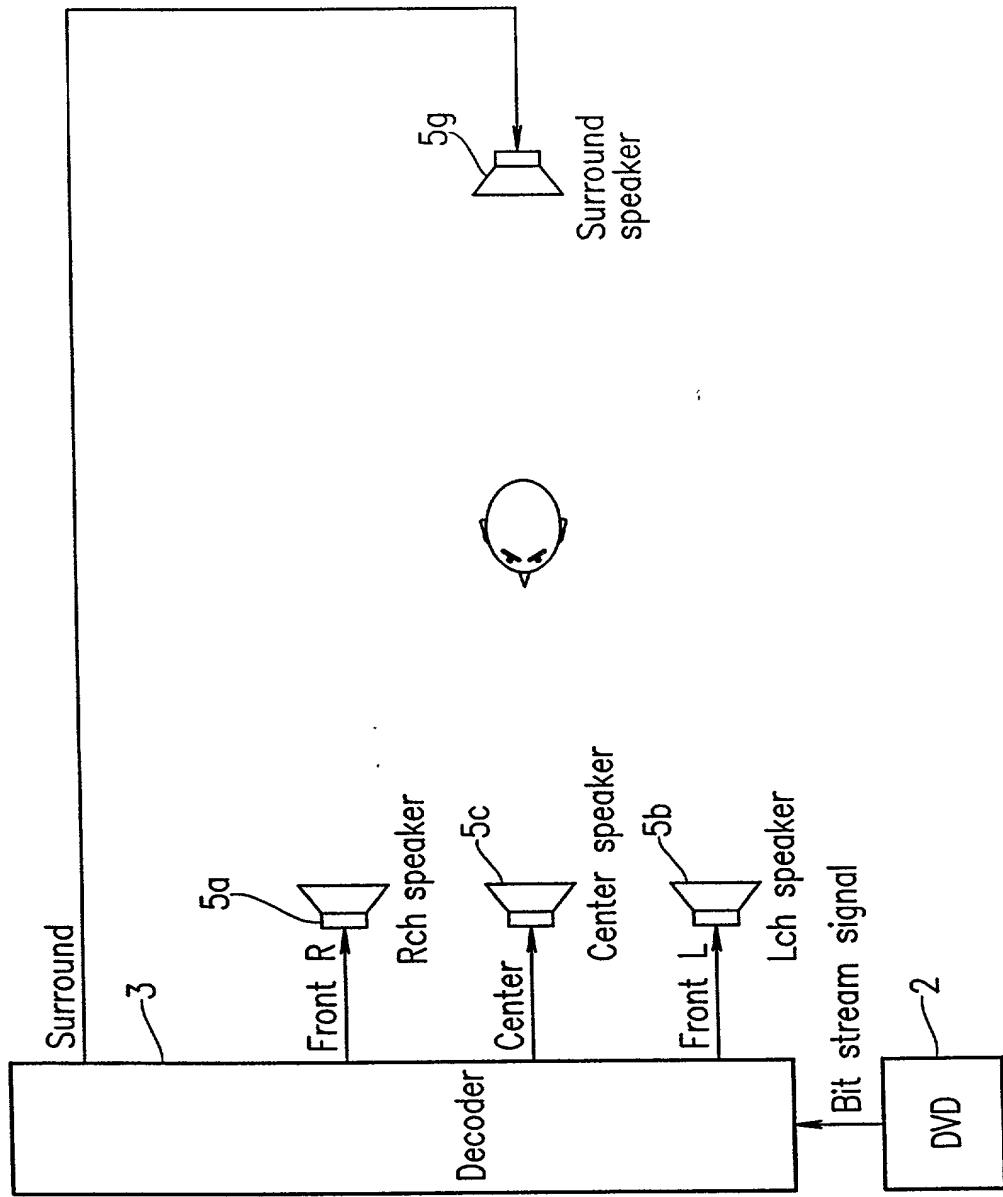
**FIG. 1.1**

FIG. 12

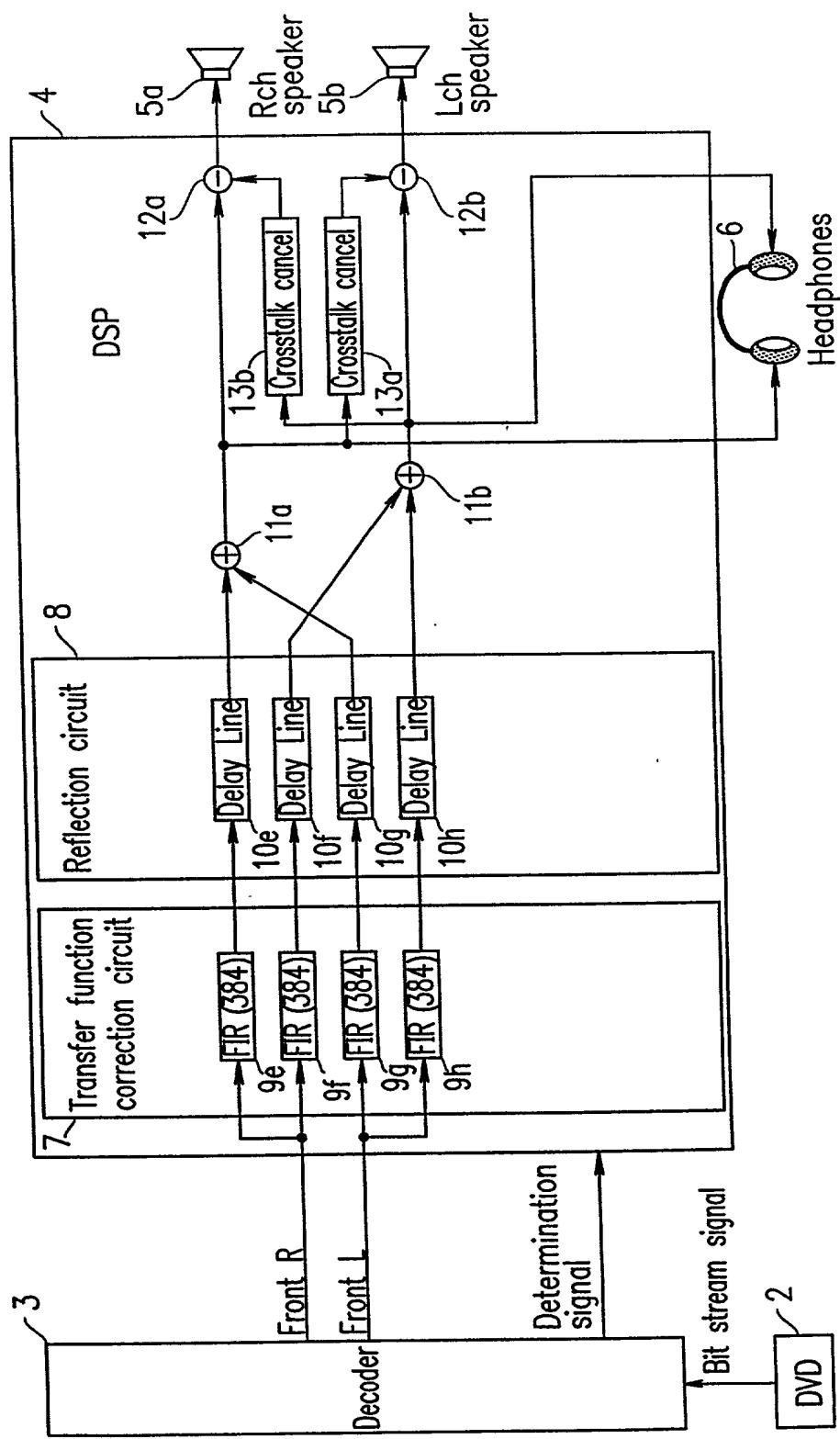


FIG. 13

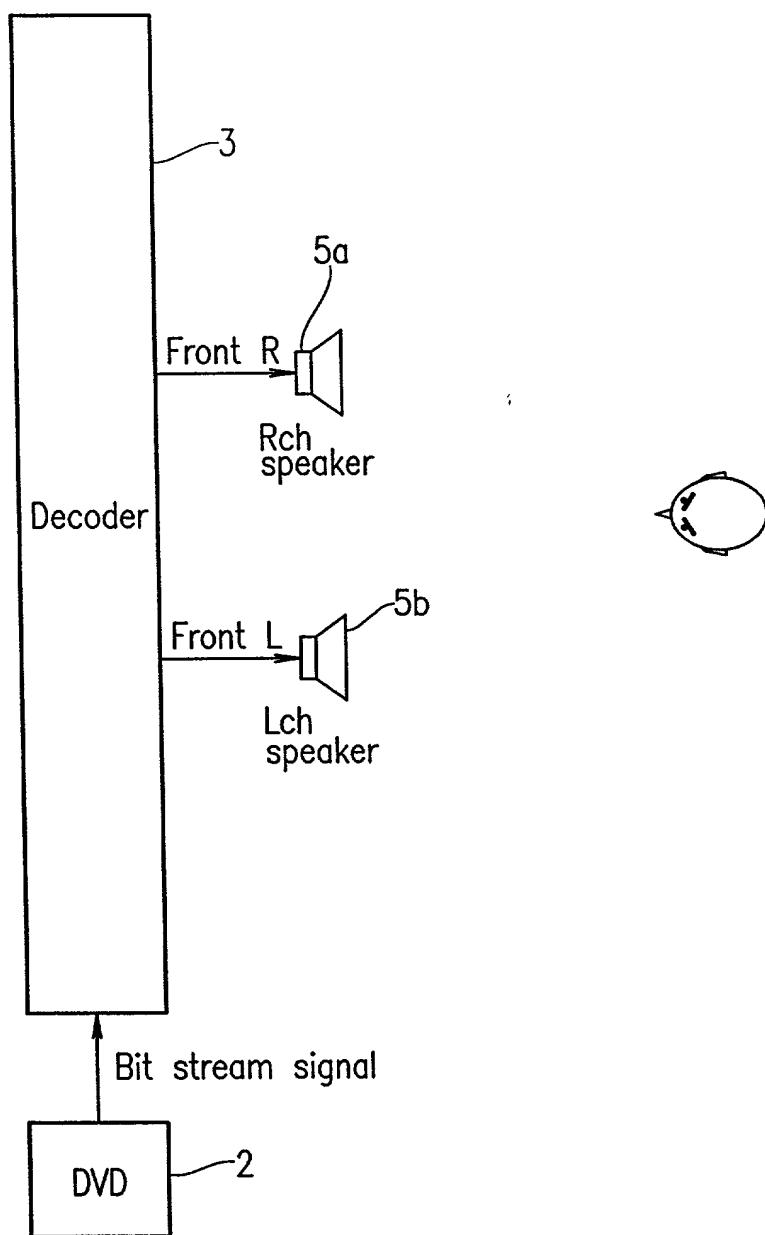


FIG. 14

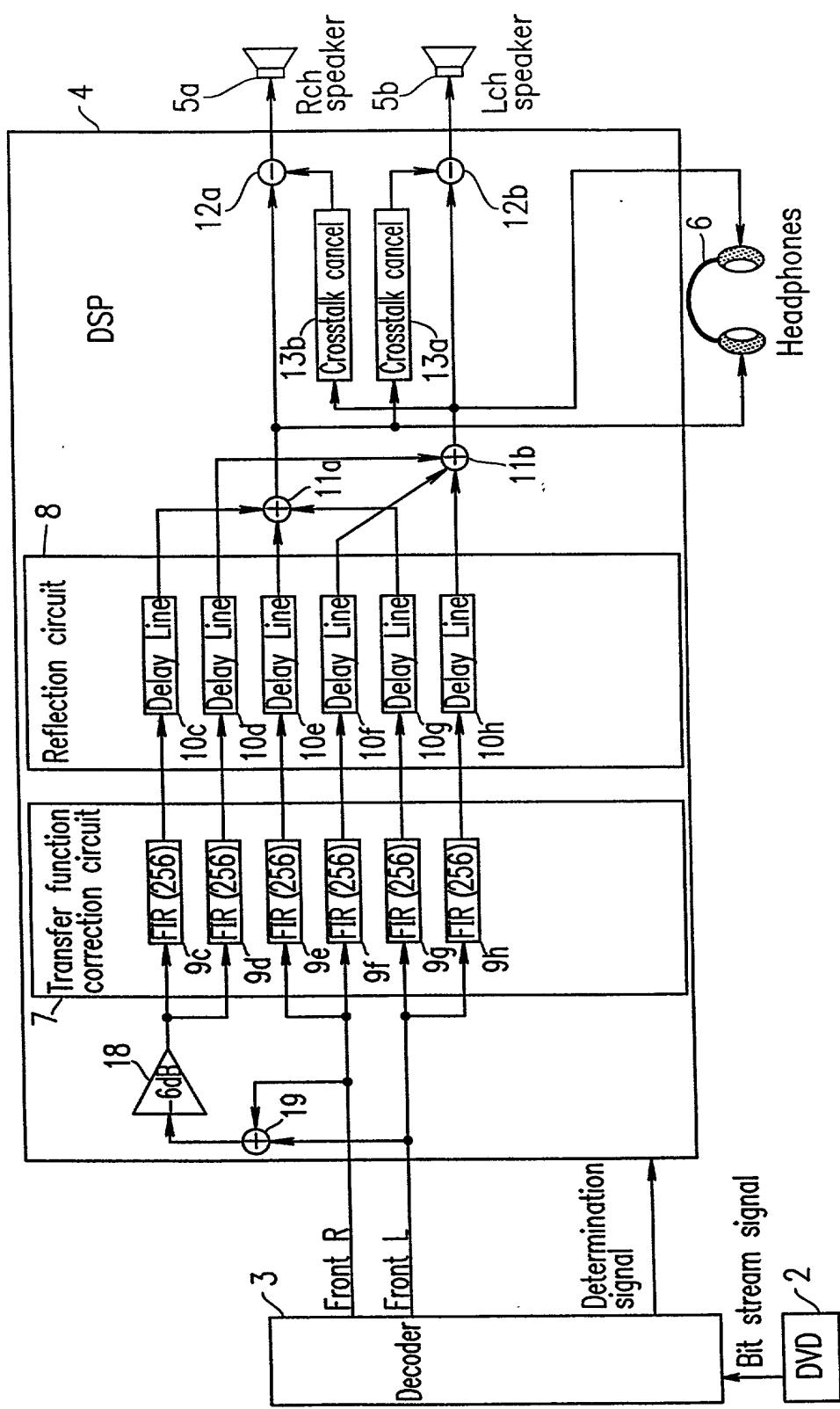


FIG. 15

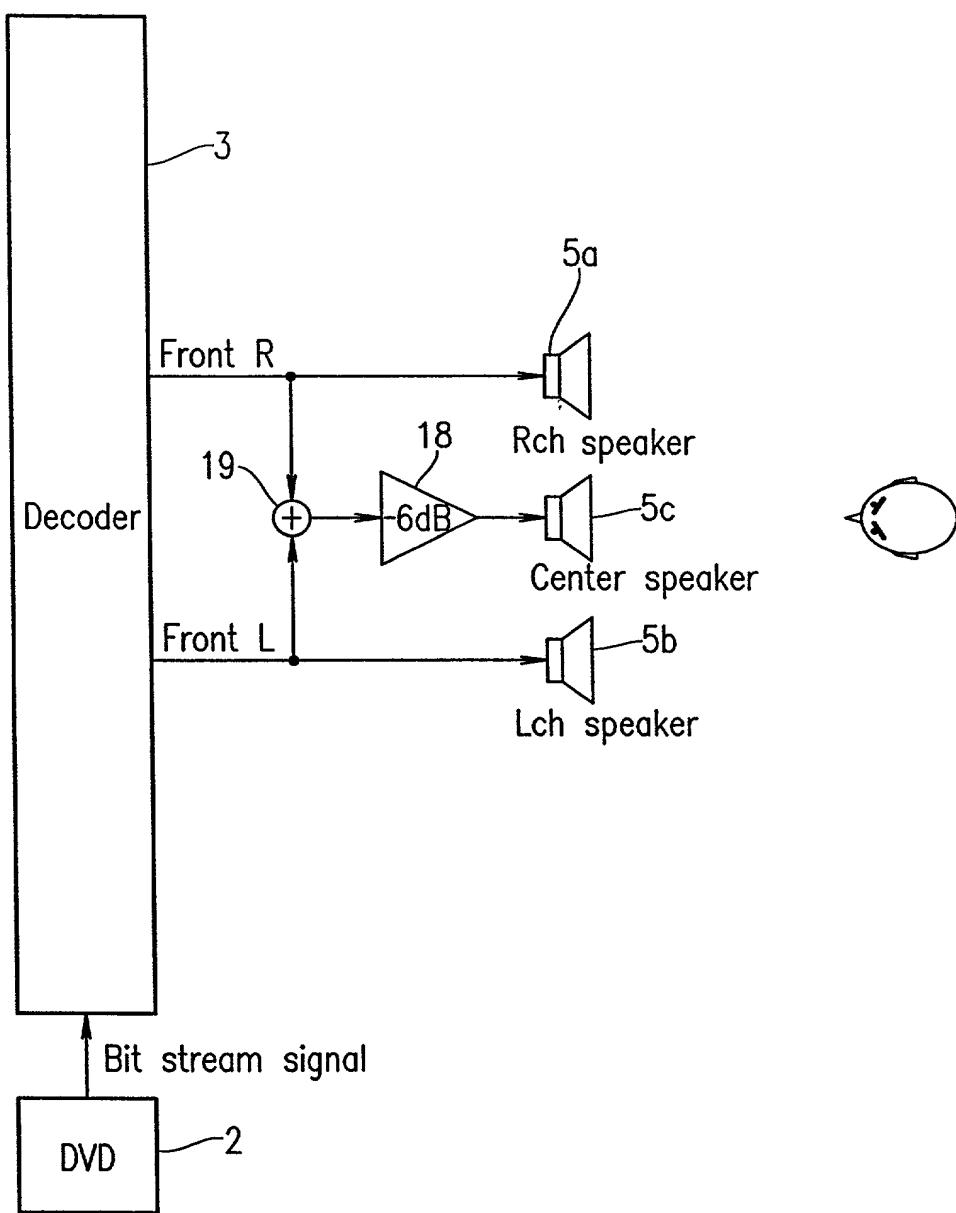


FIG. 16

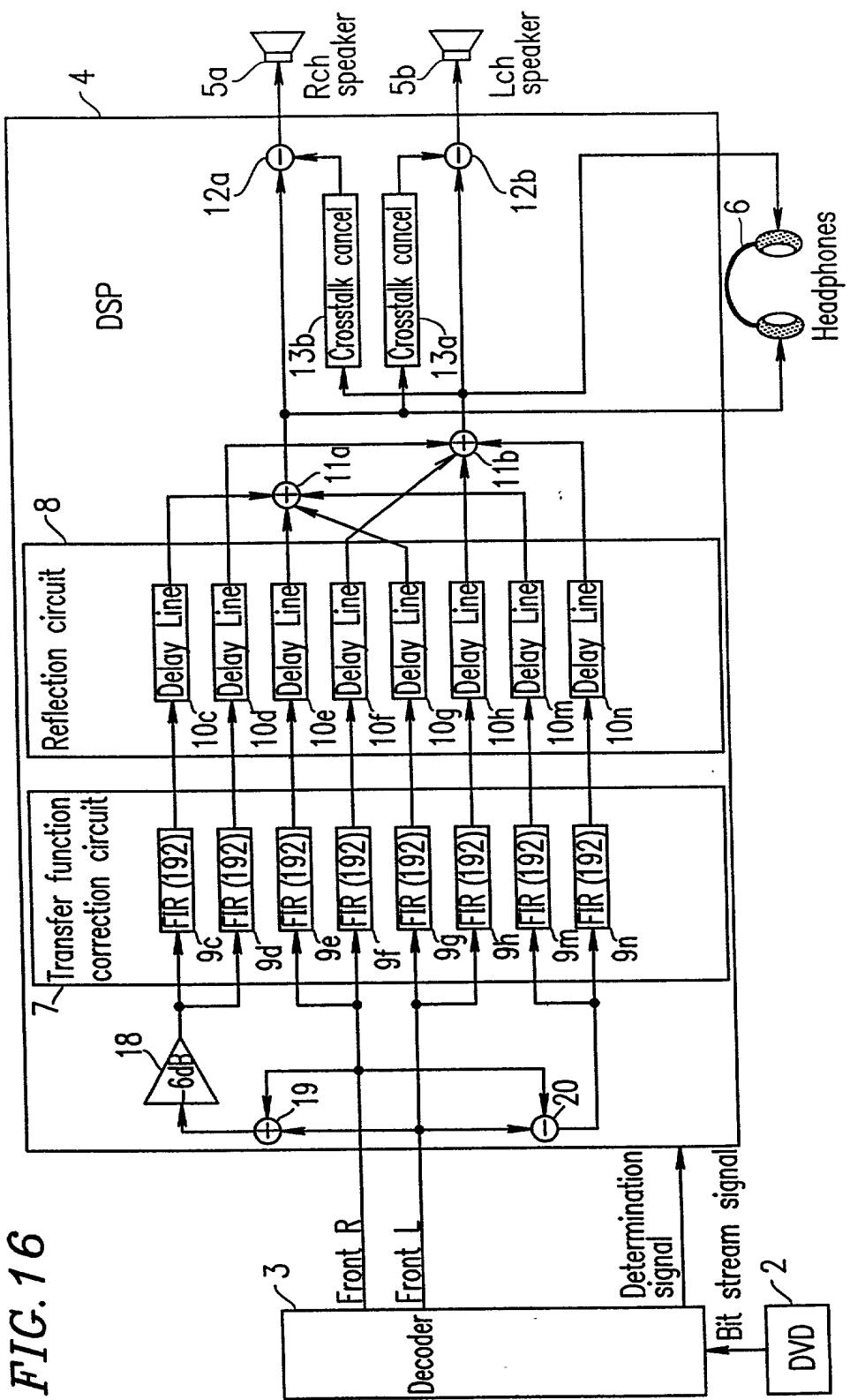
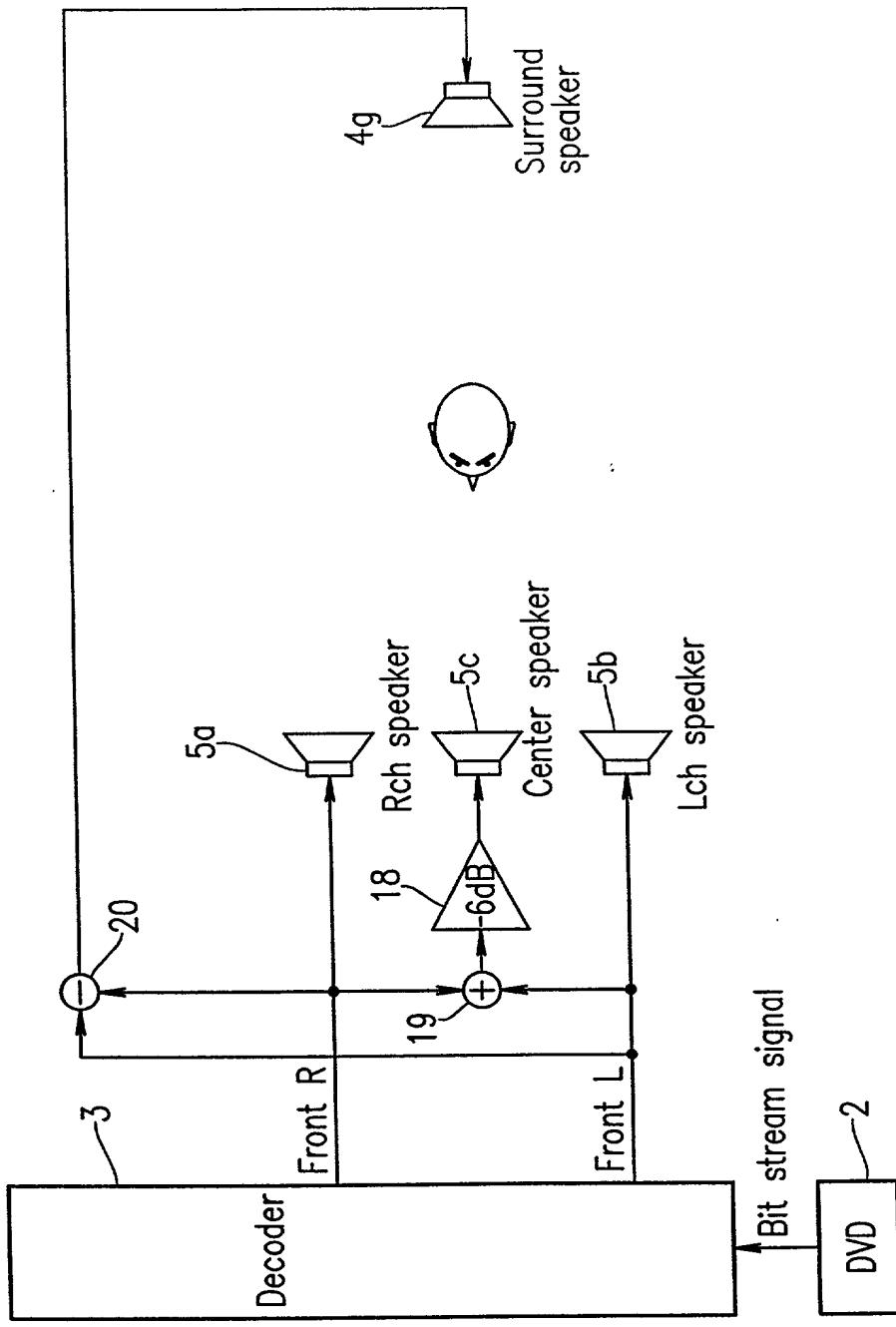


FIG. 17



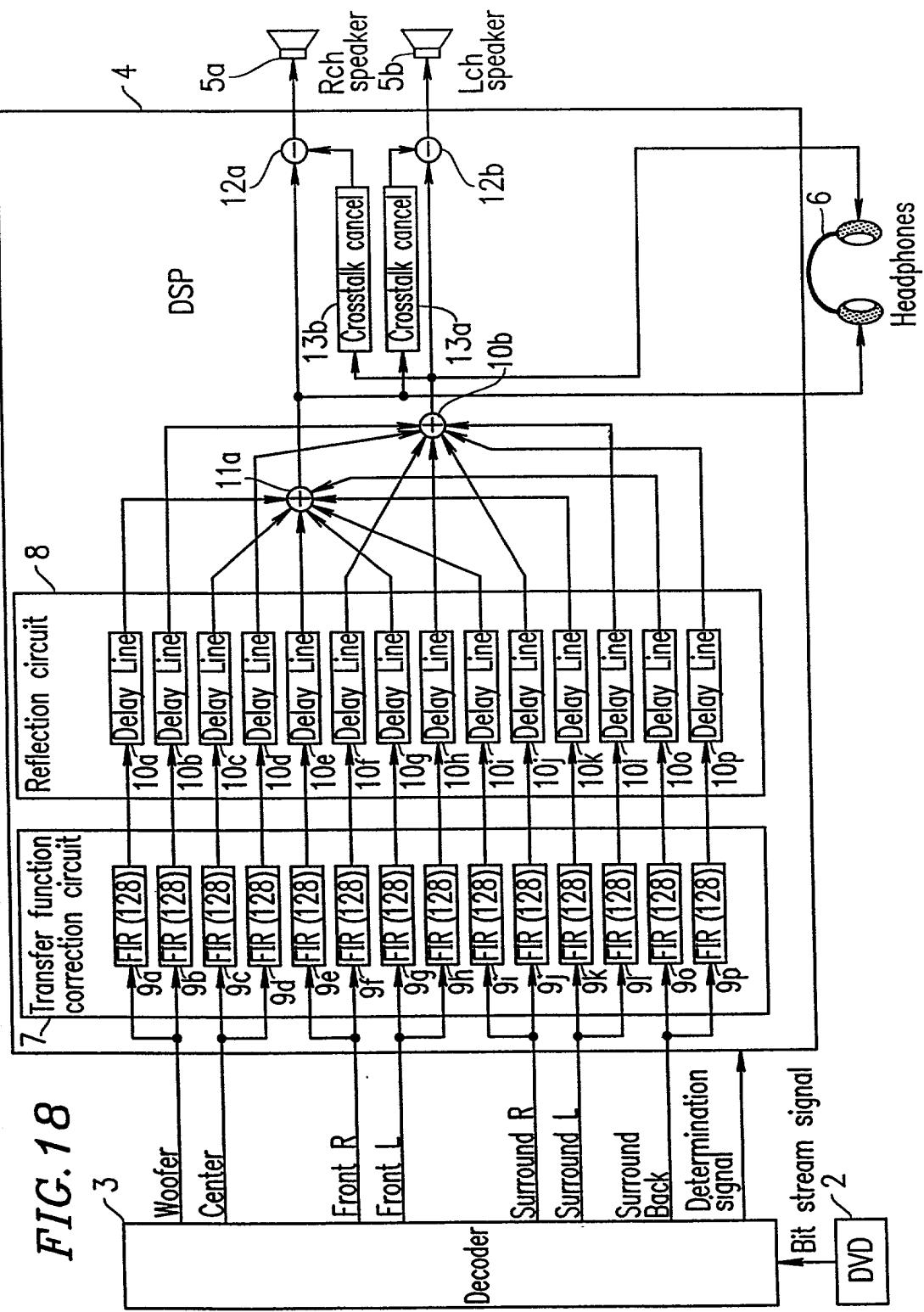
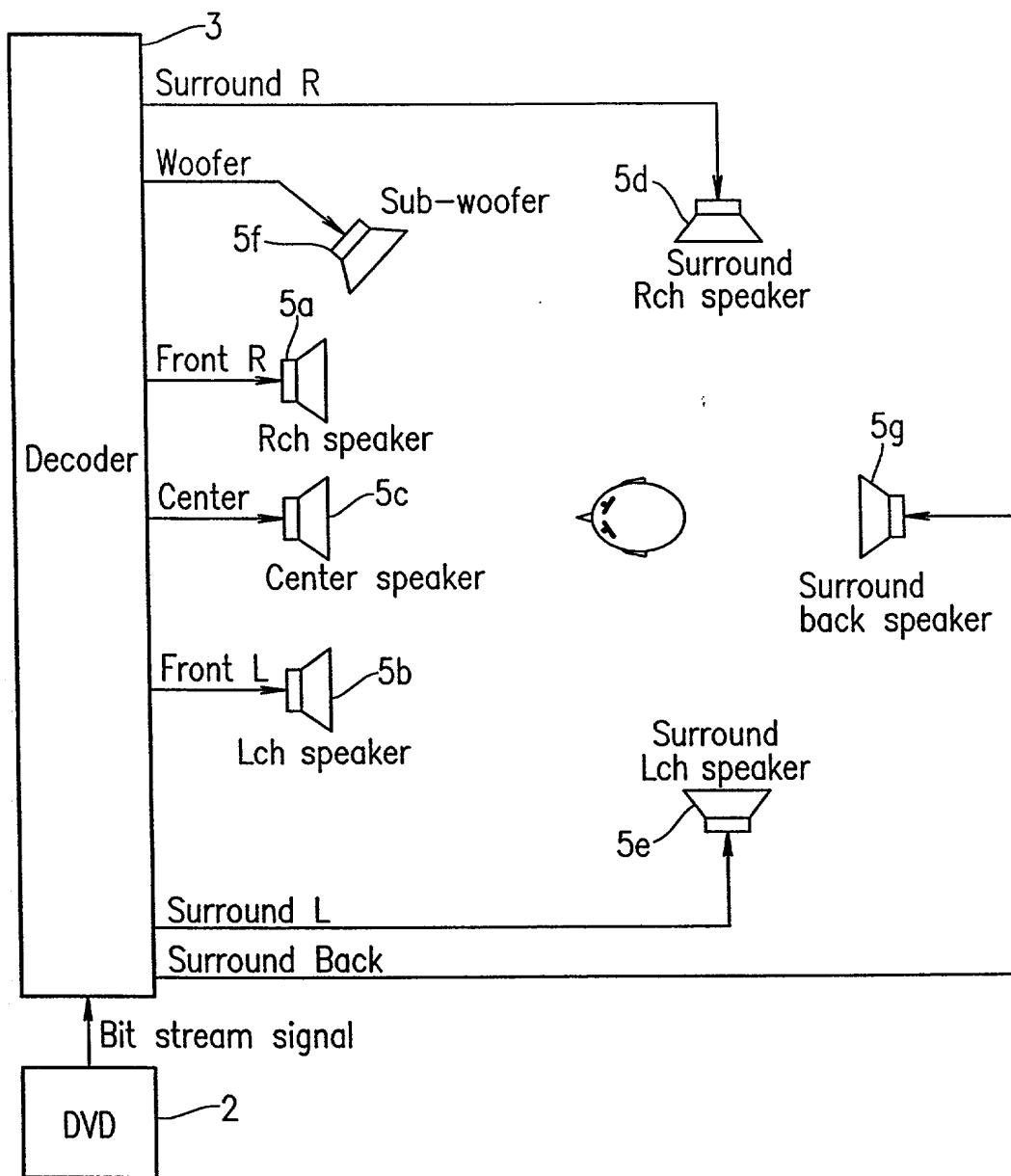


FIG. 19



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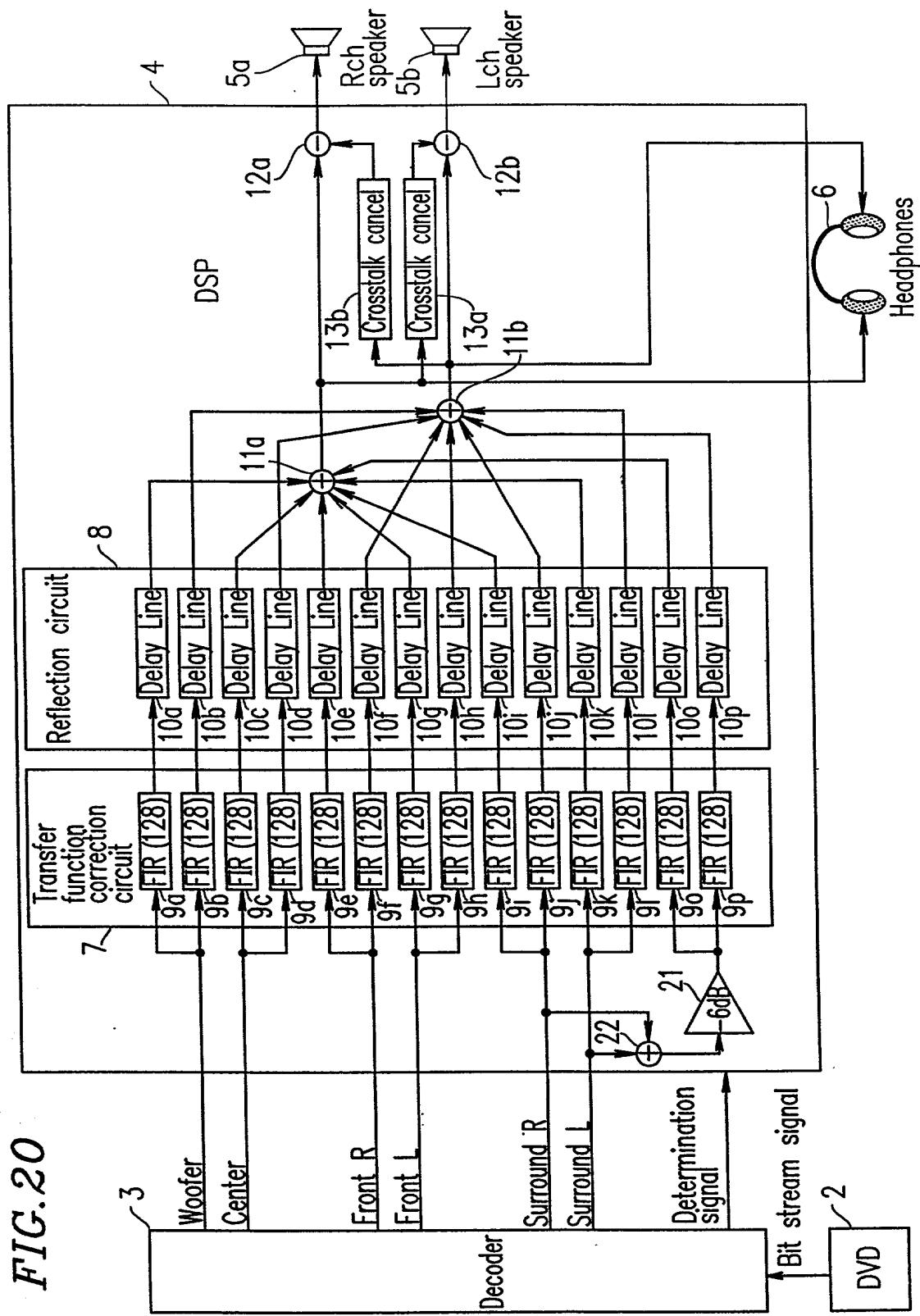


FIG. 21

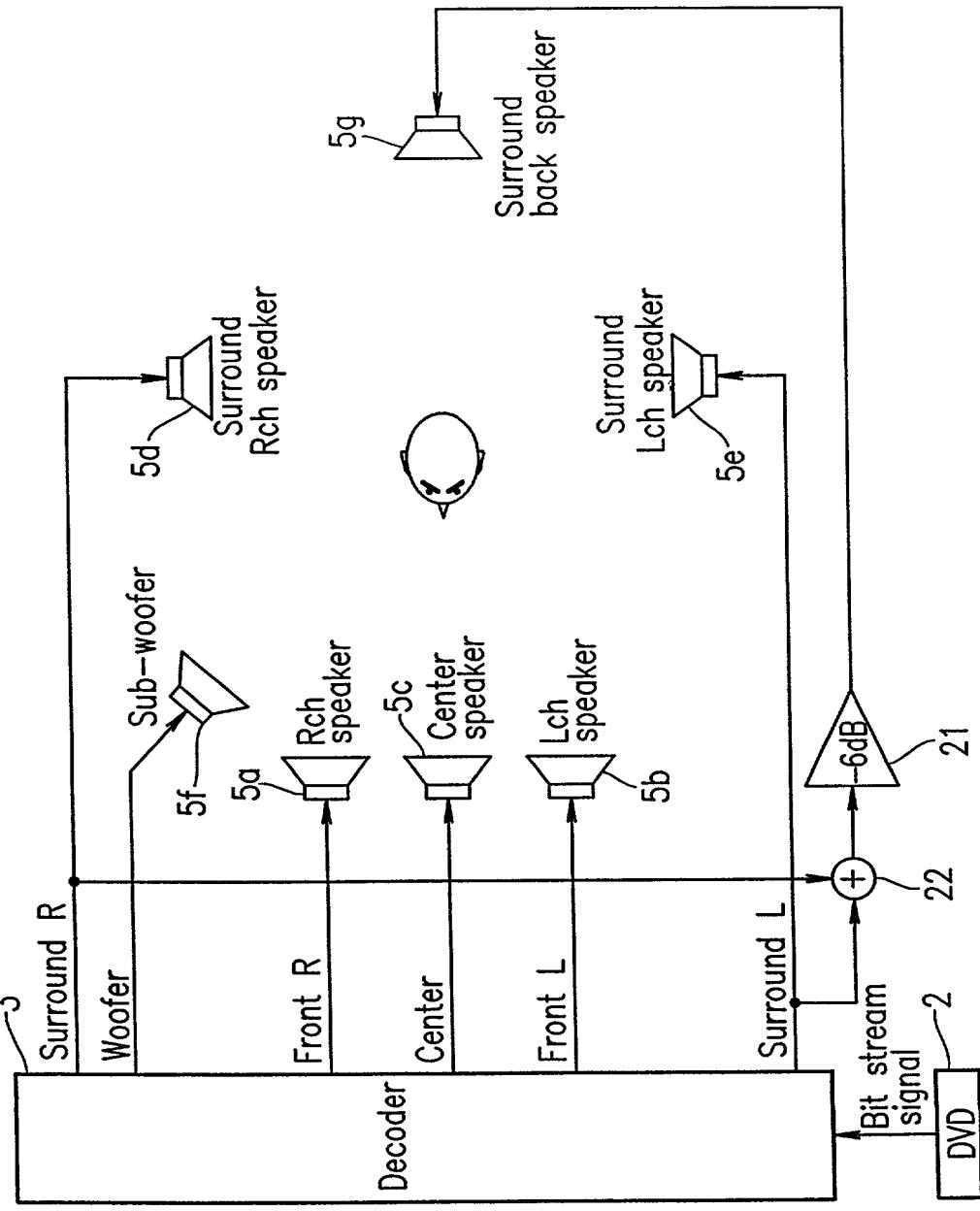


FIG.22

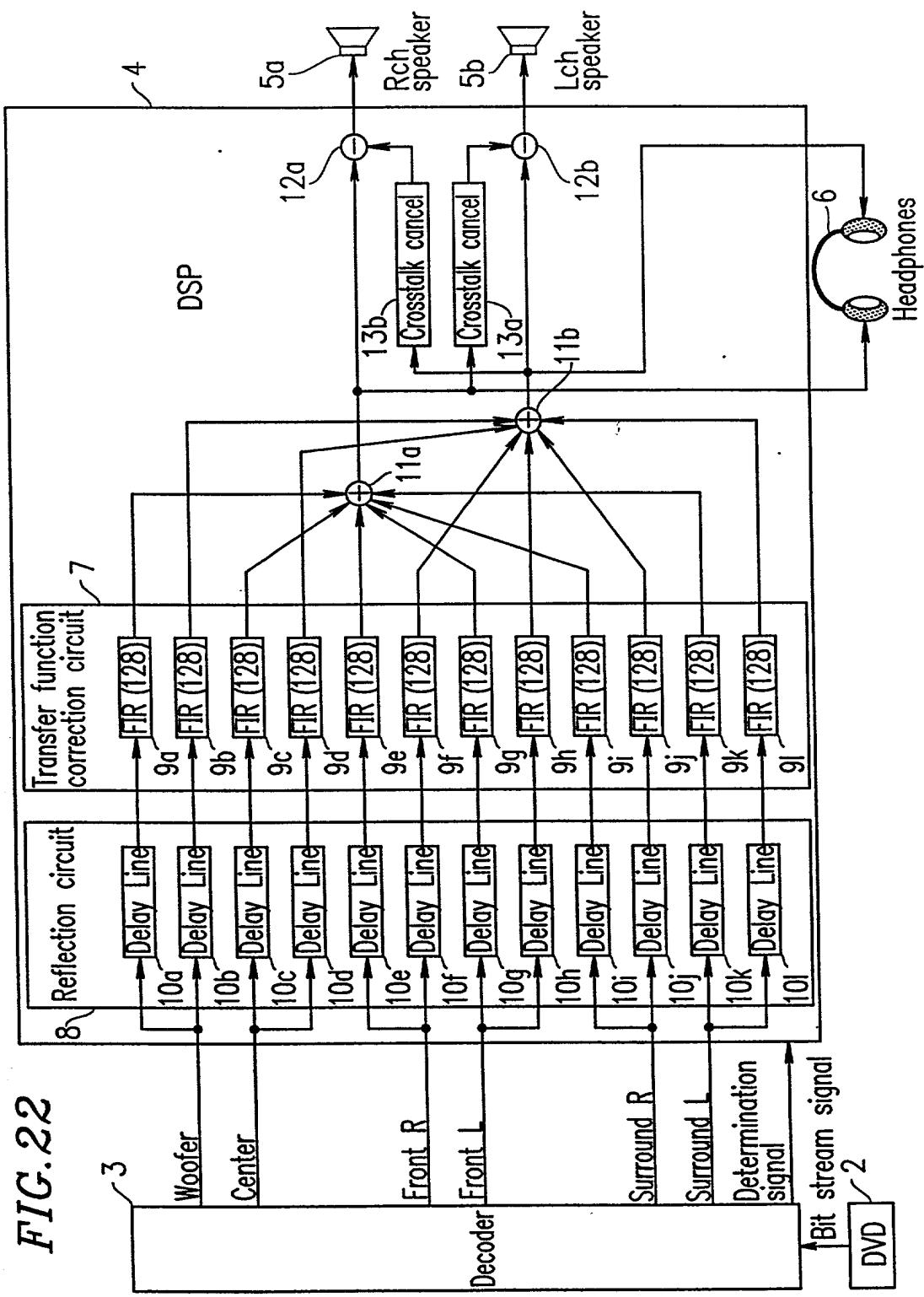


FIG.23

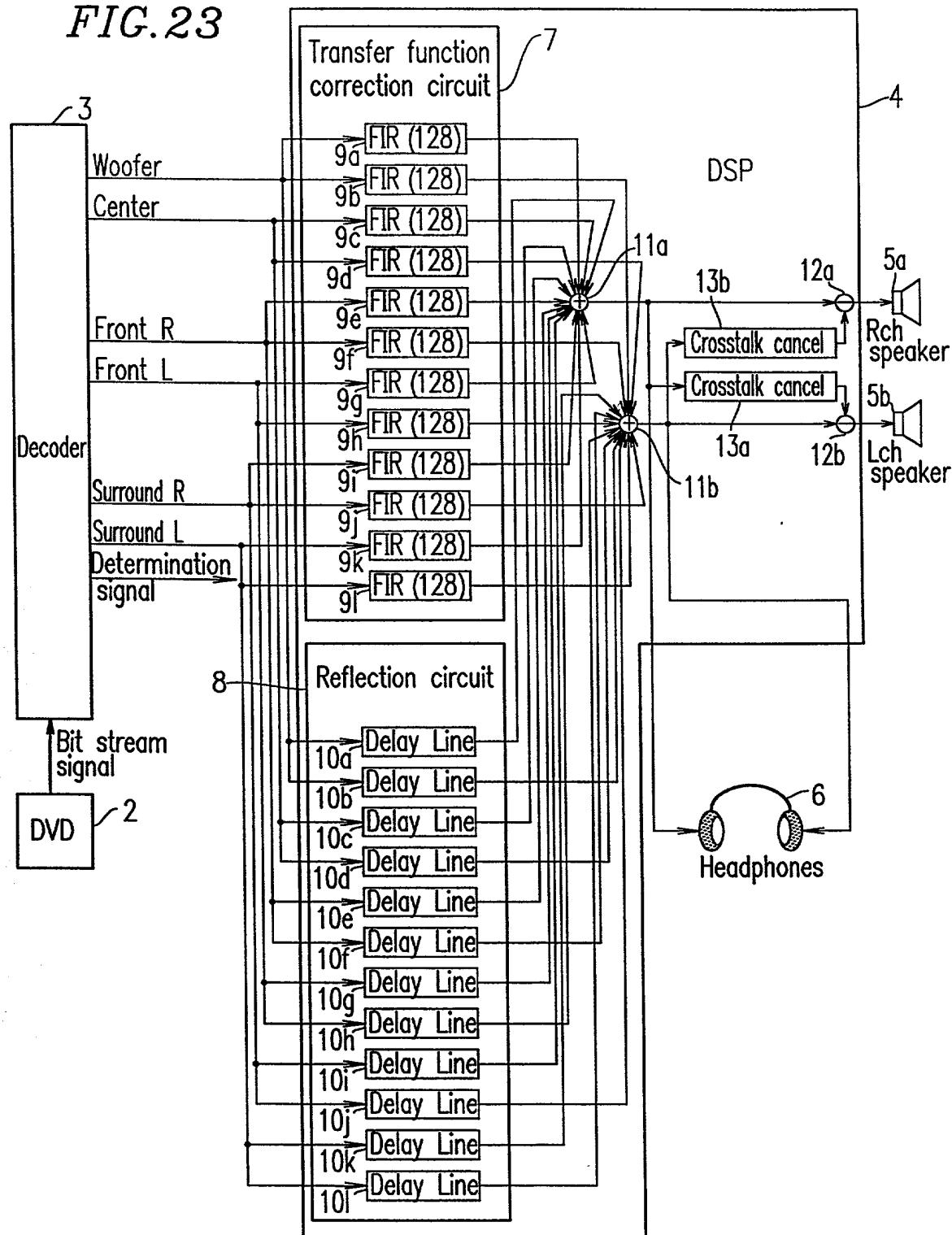
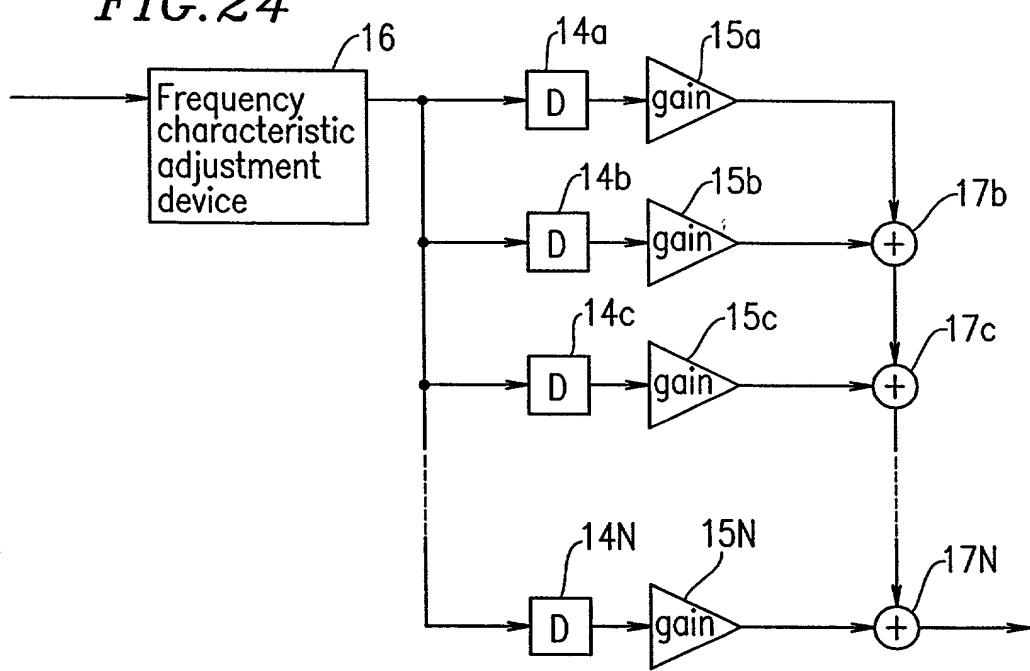
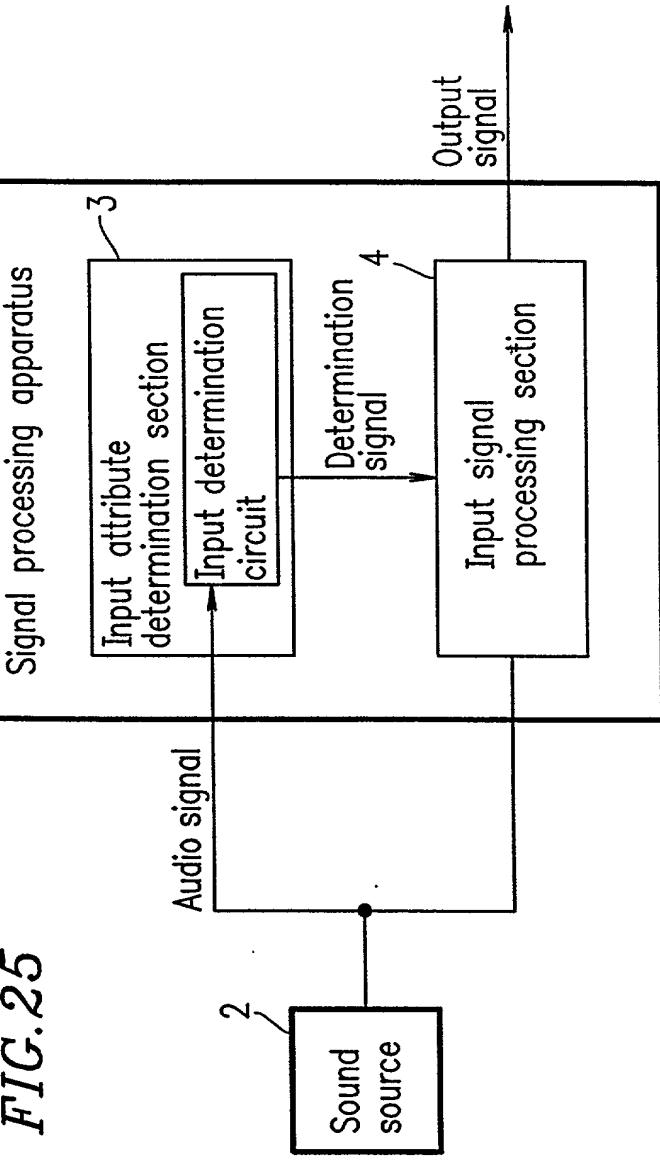


FIG.23A FIG.23B FIG.23C FIG.23D

FIG.24





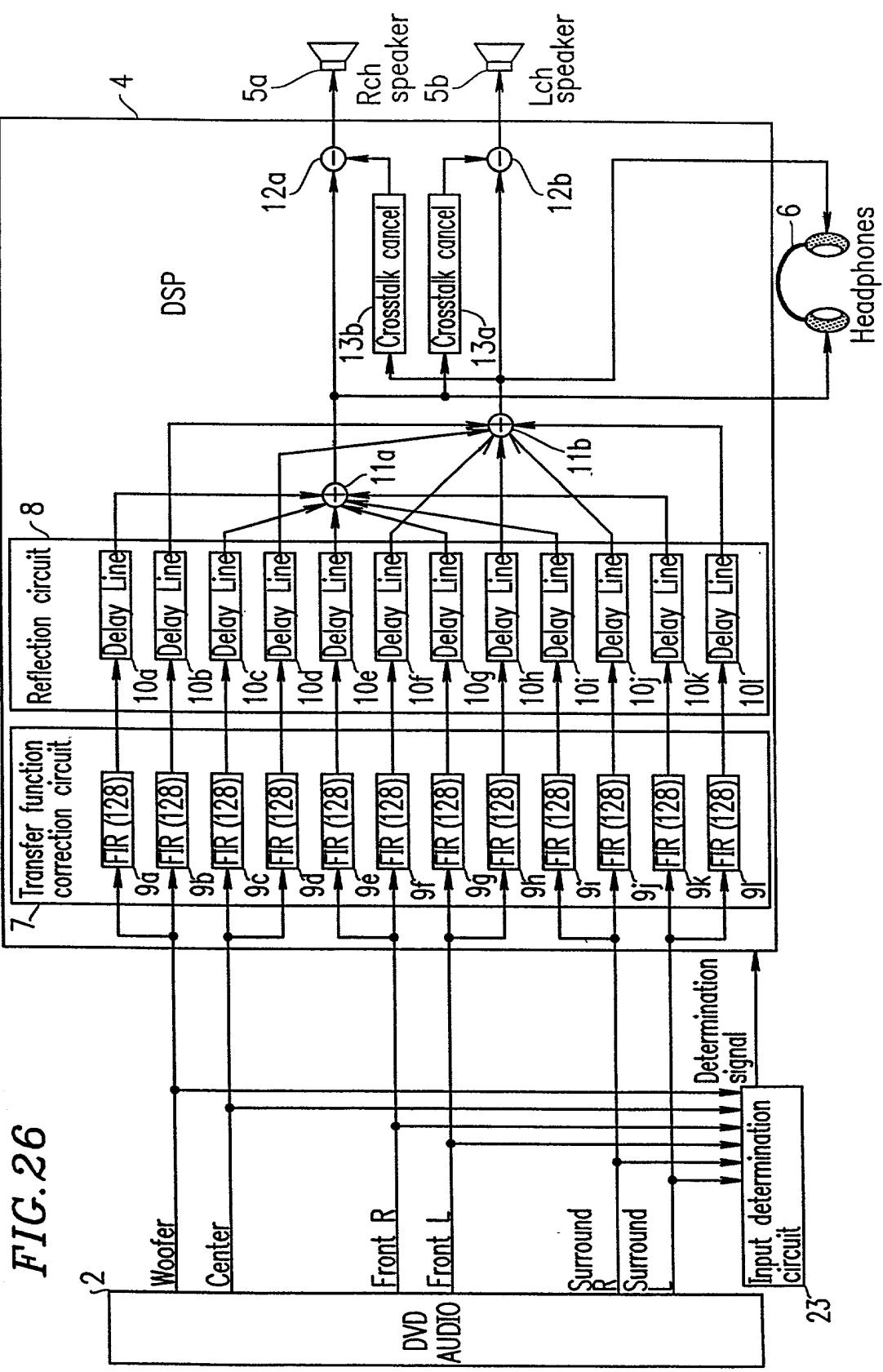
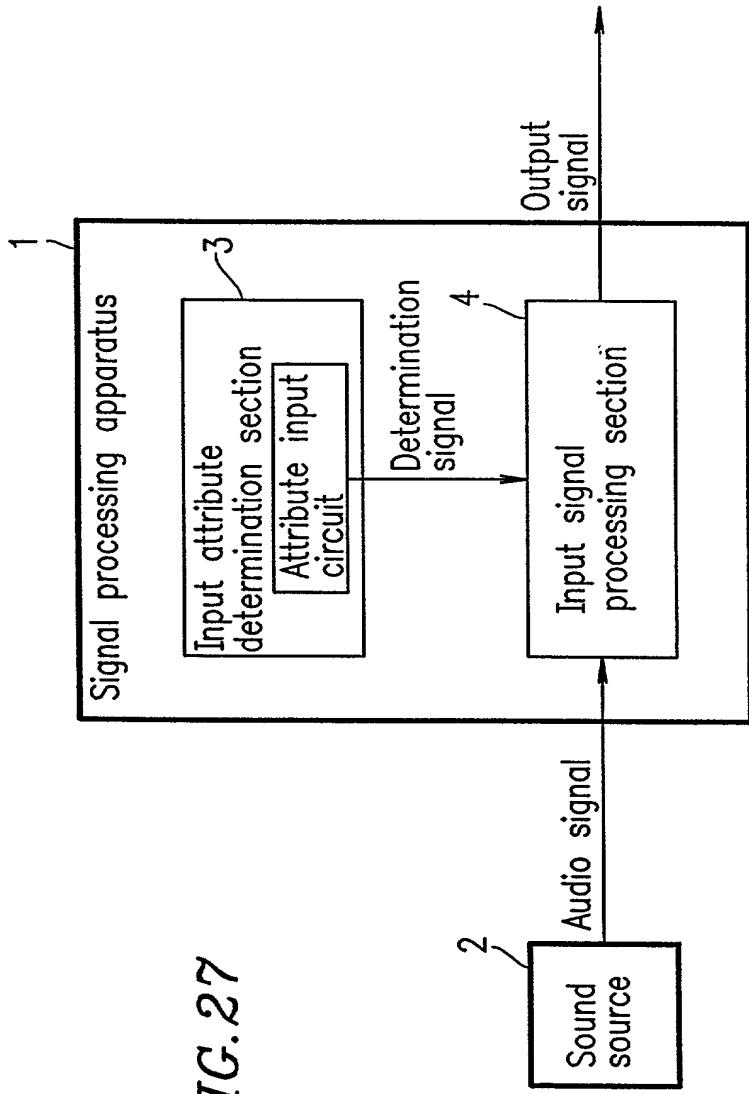
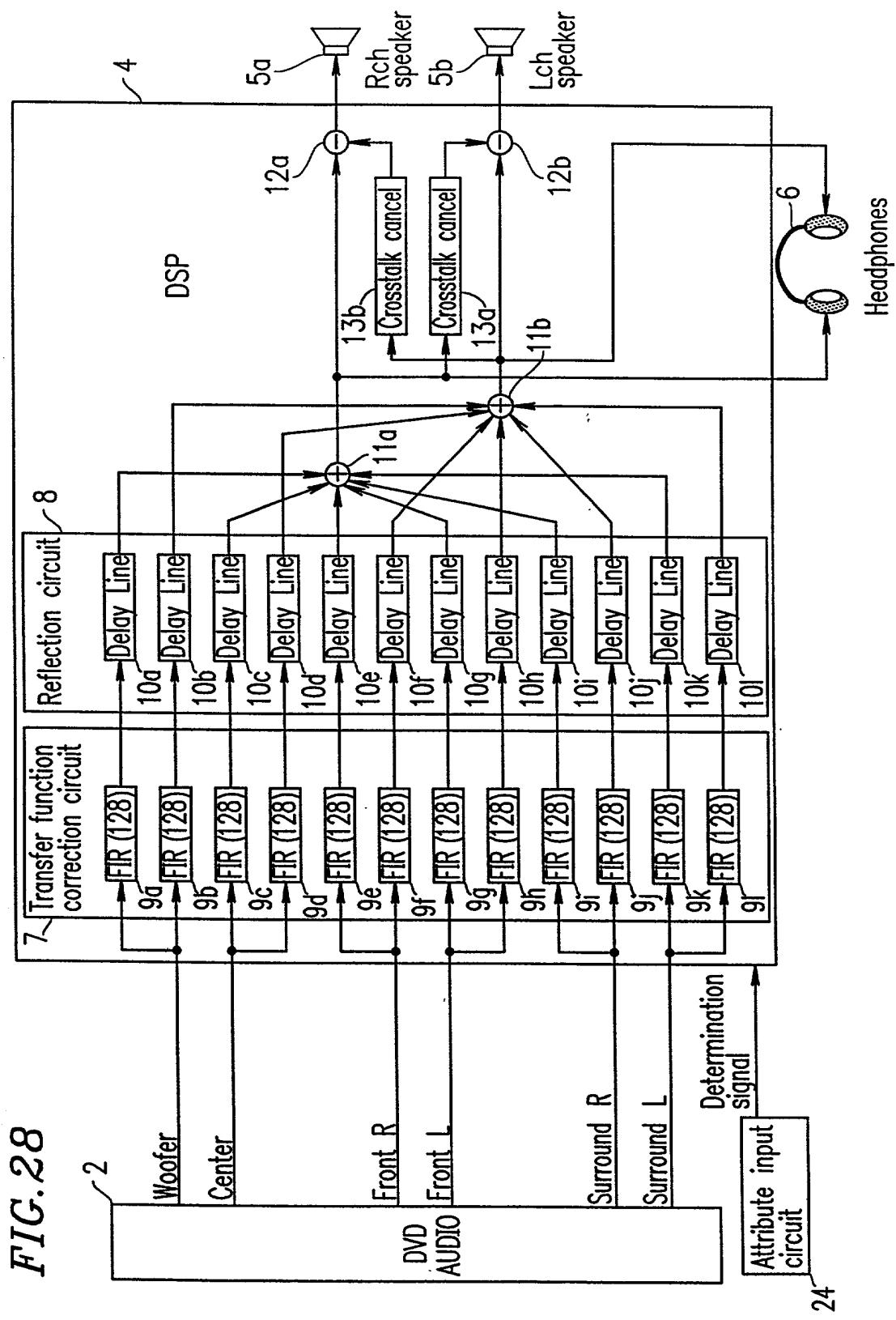


FIG. 27





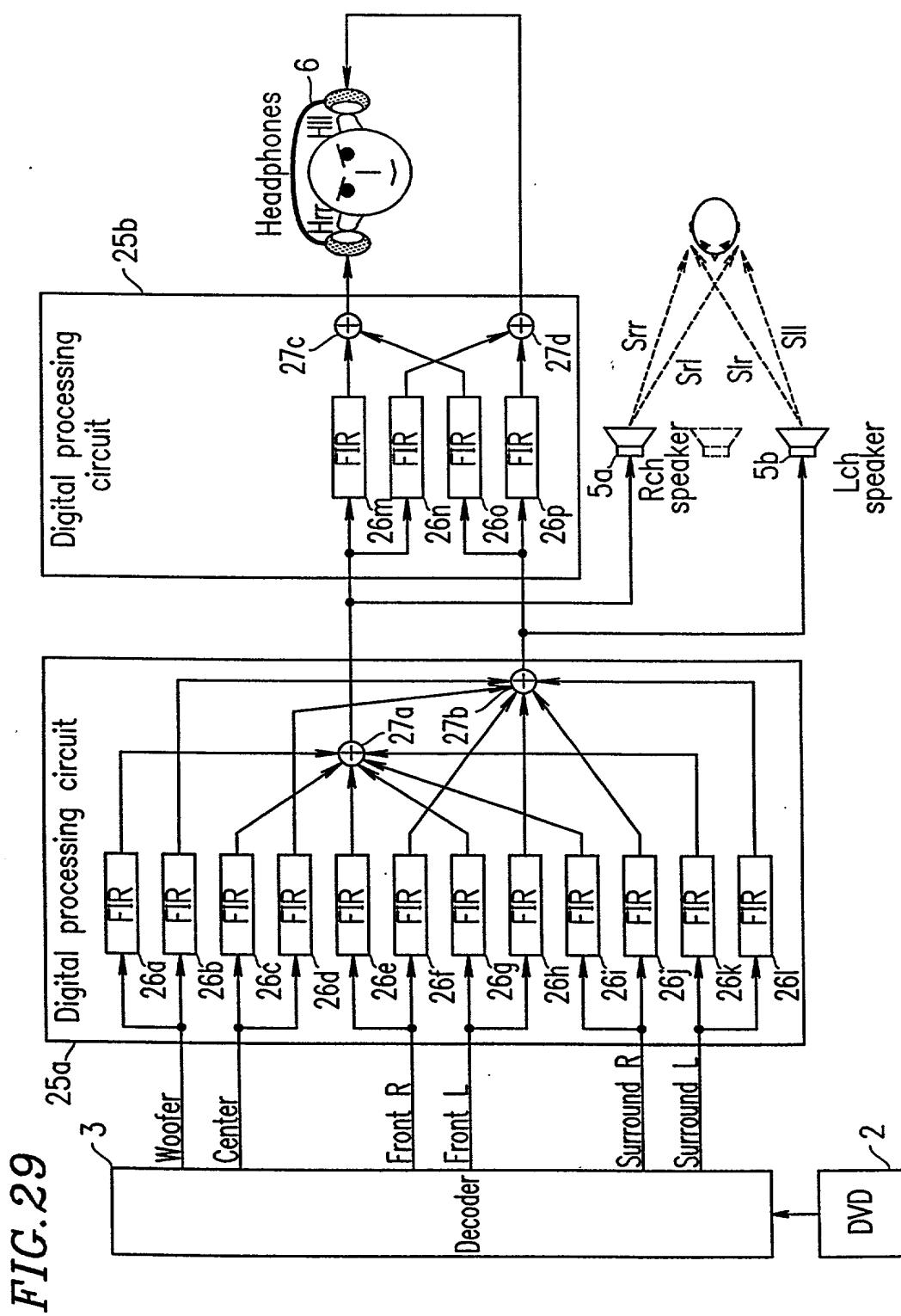
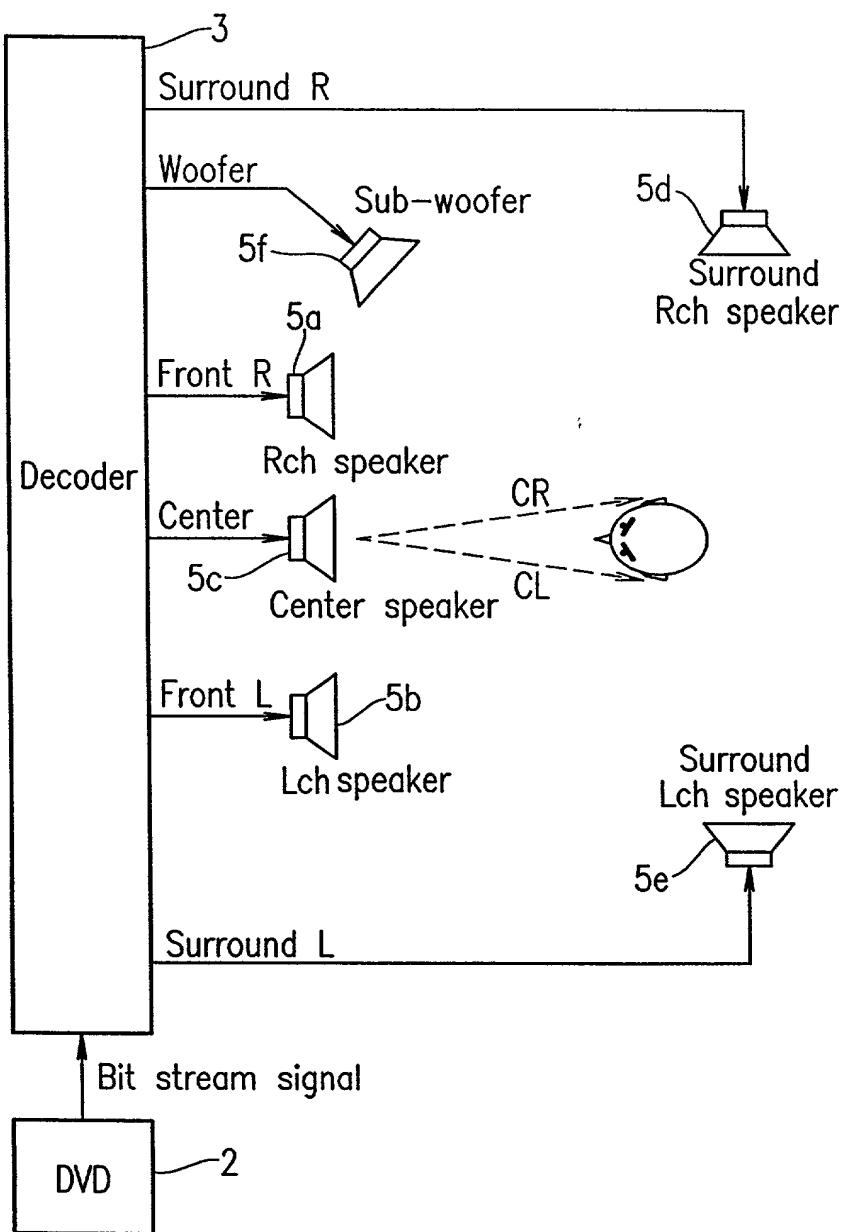
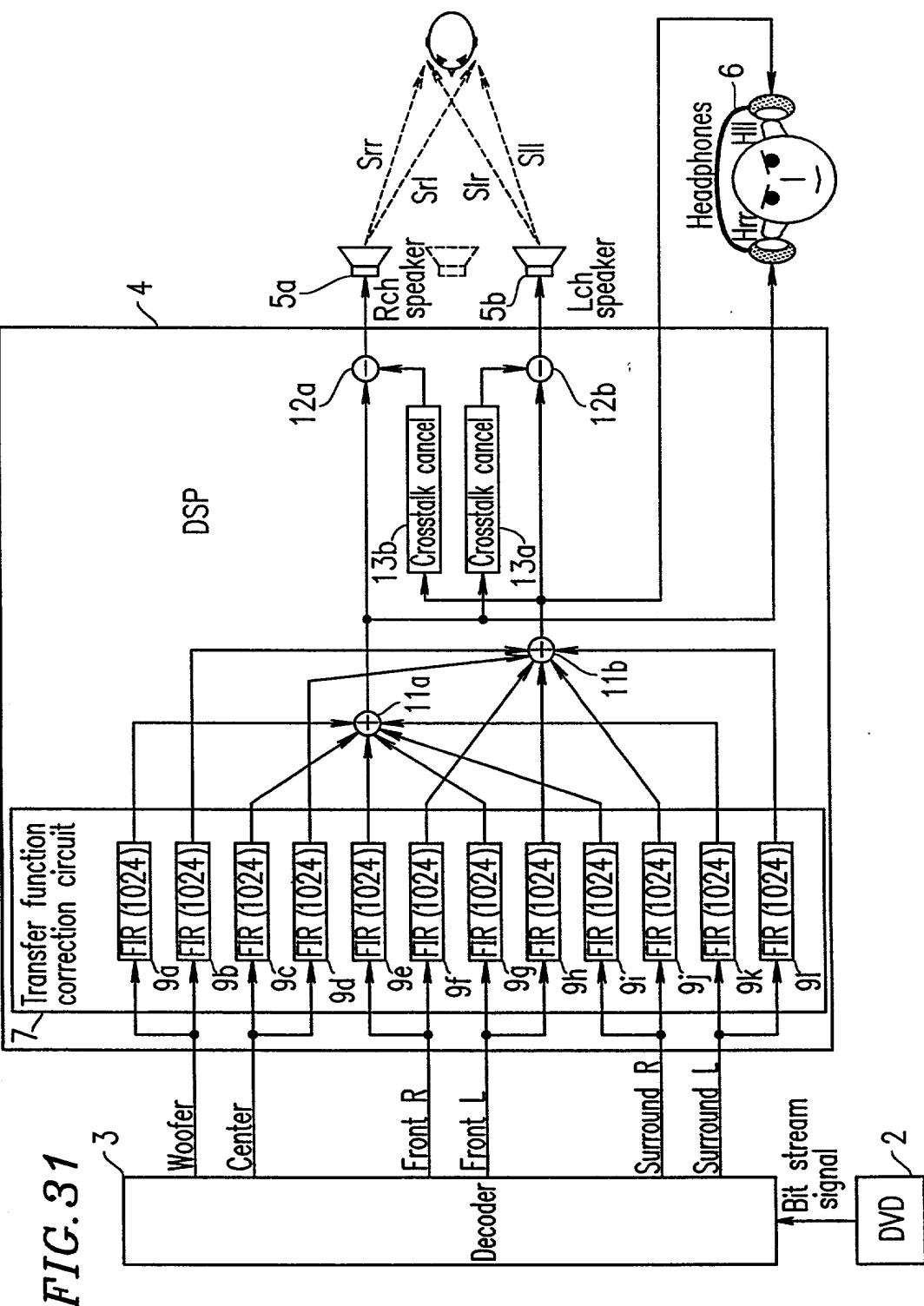


FIG.30





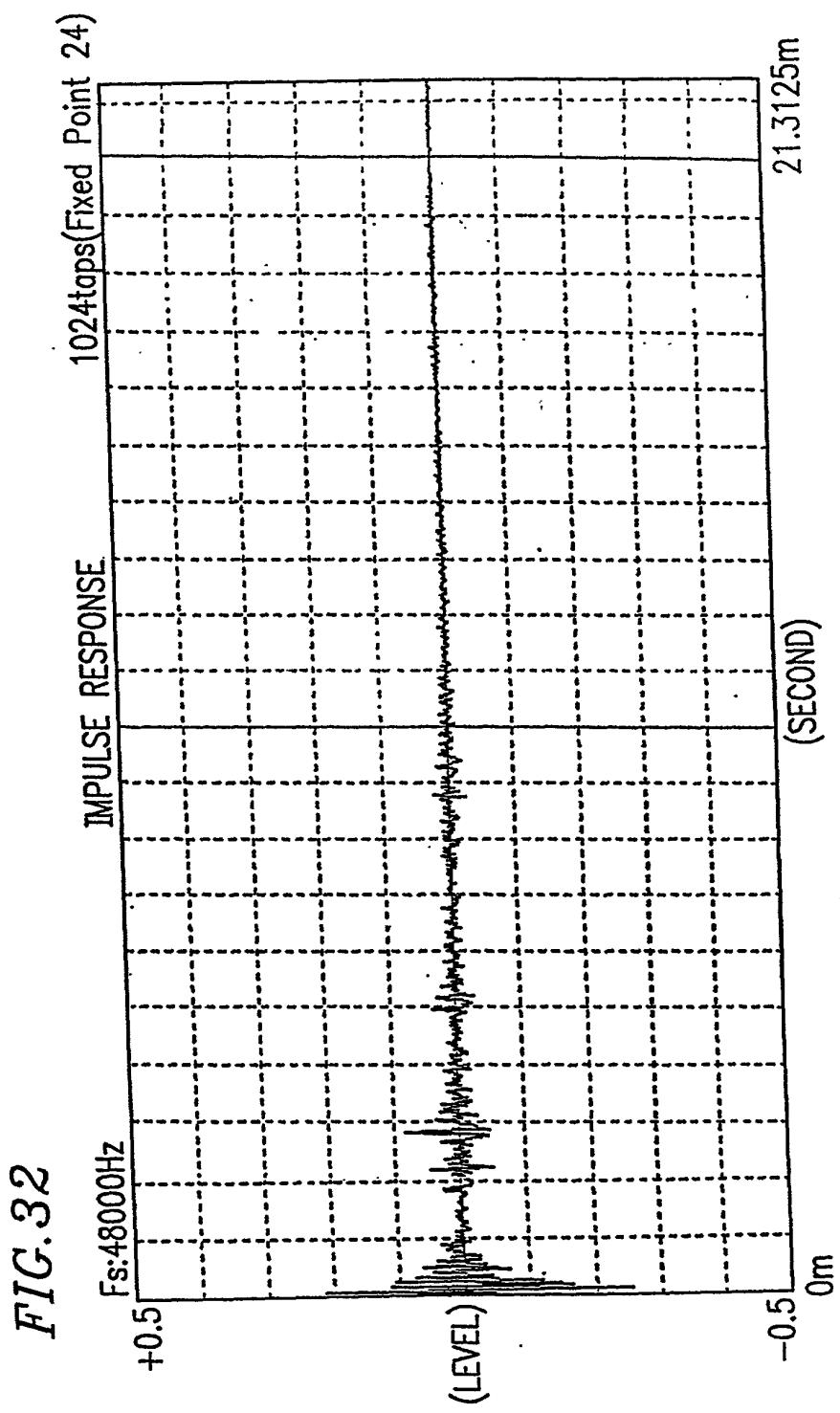
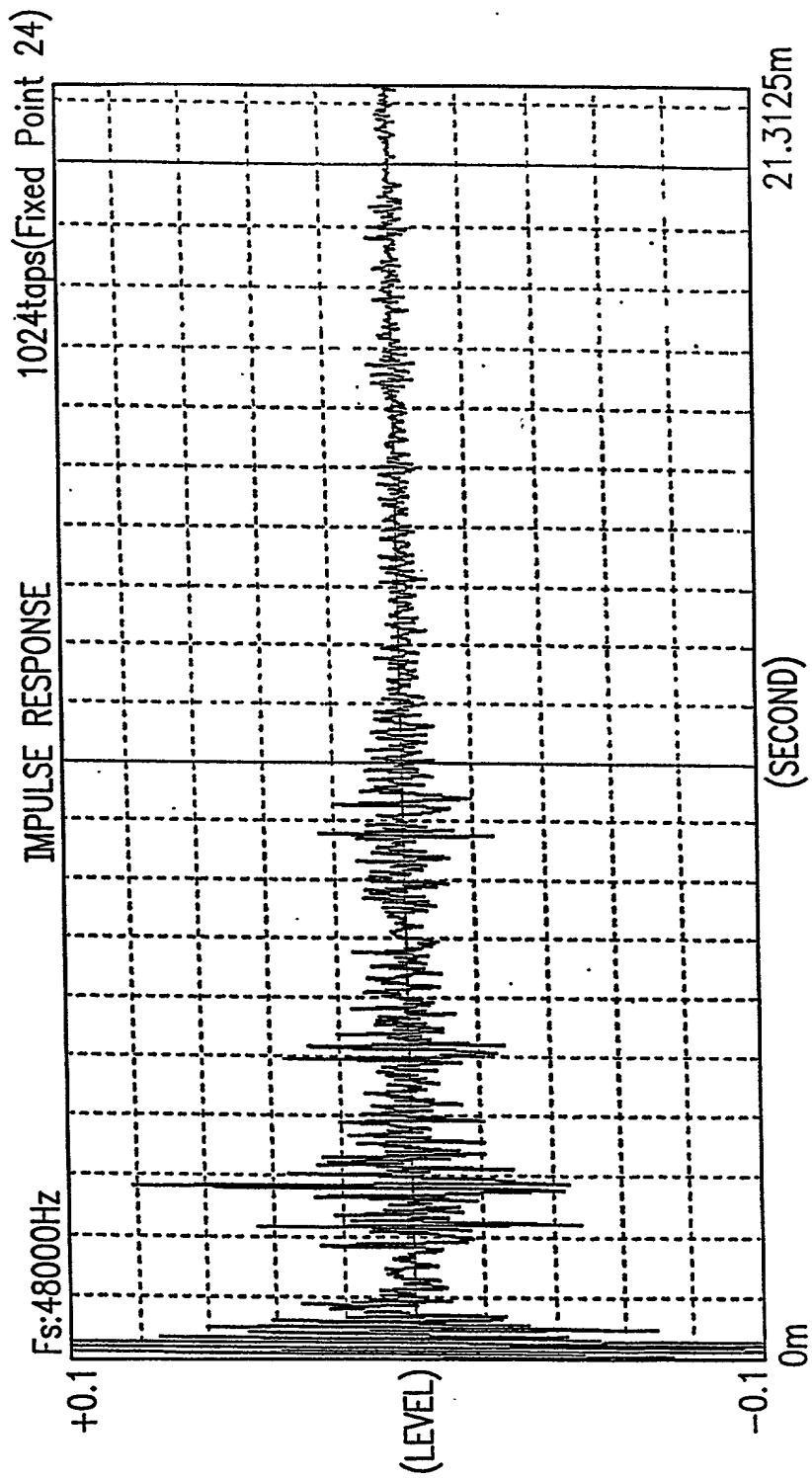


FIG. 33



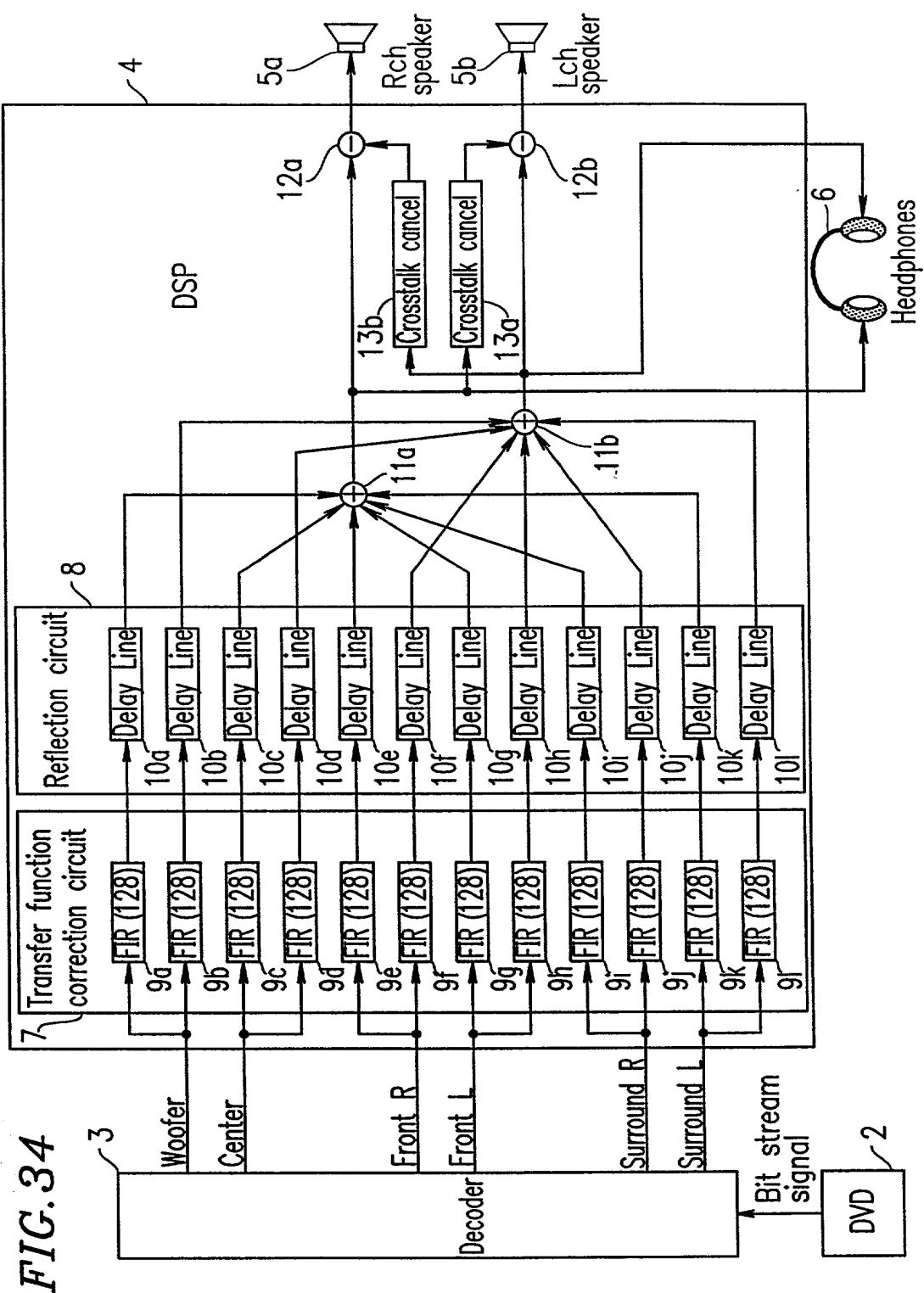


FIG.35

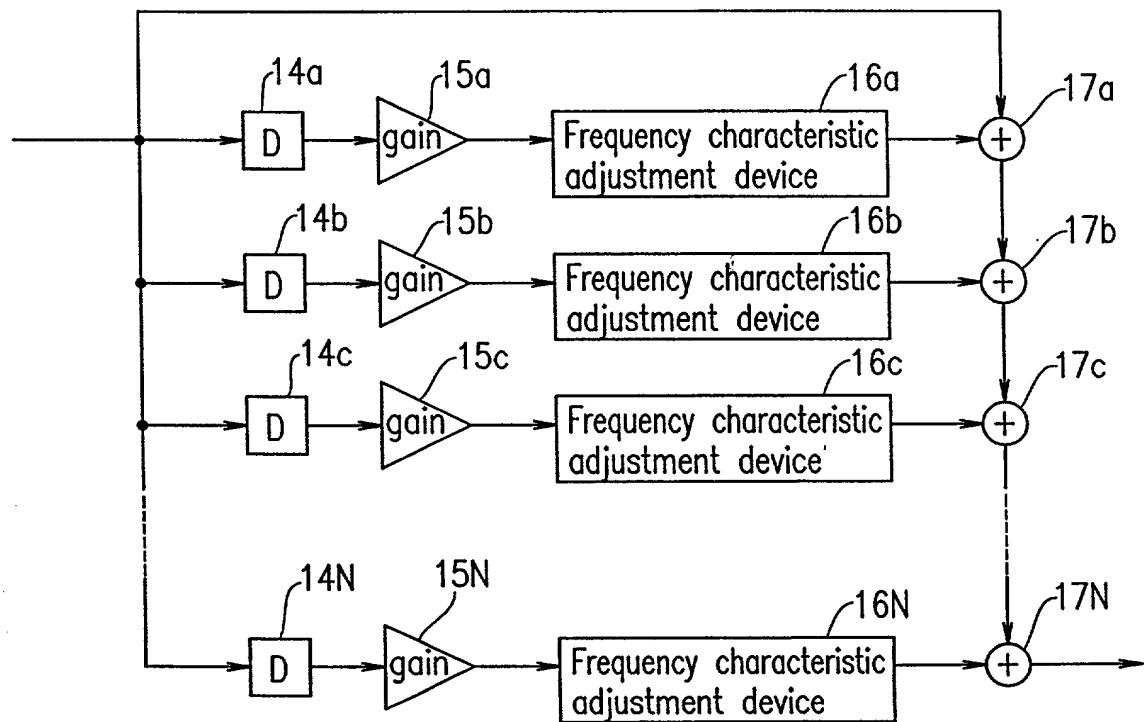
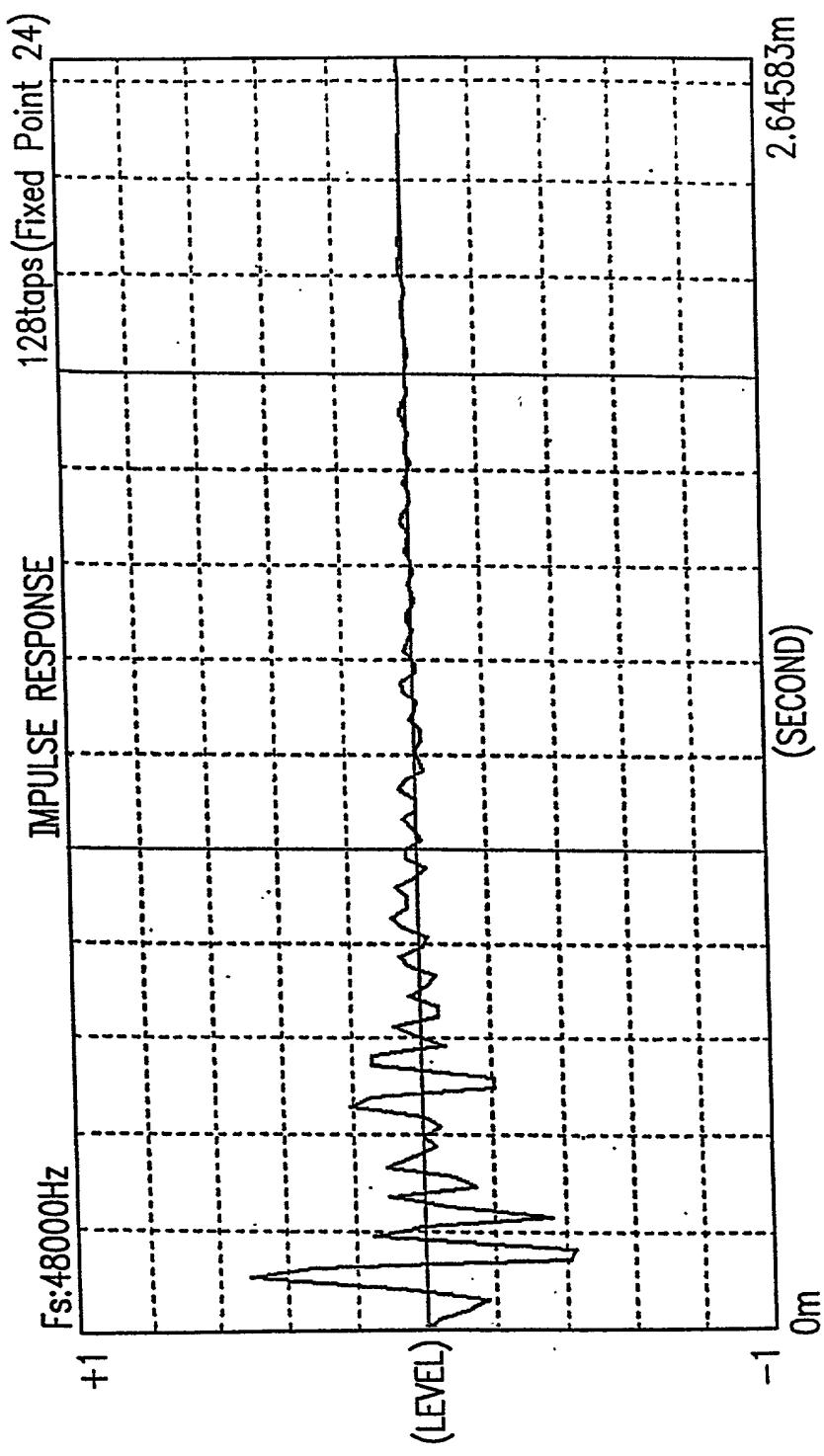


FIG. 36



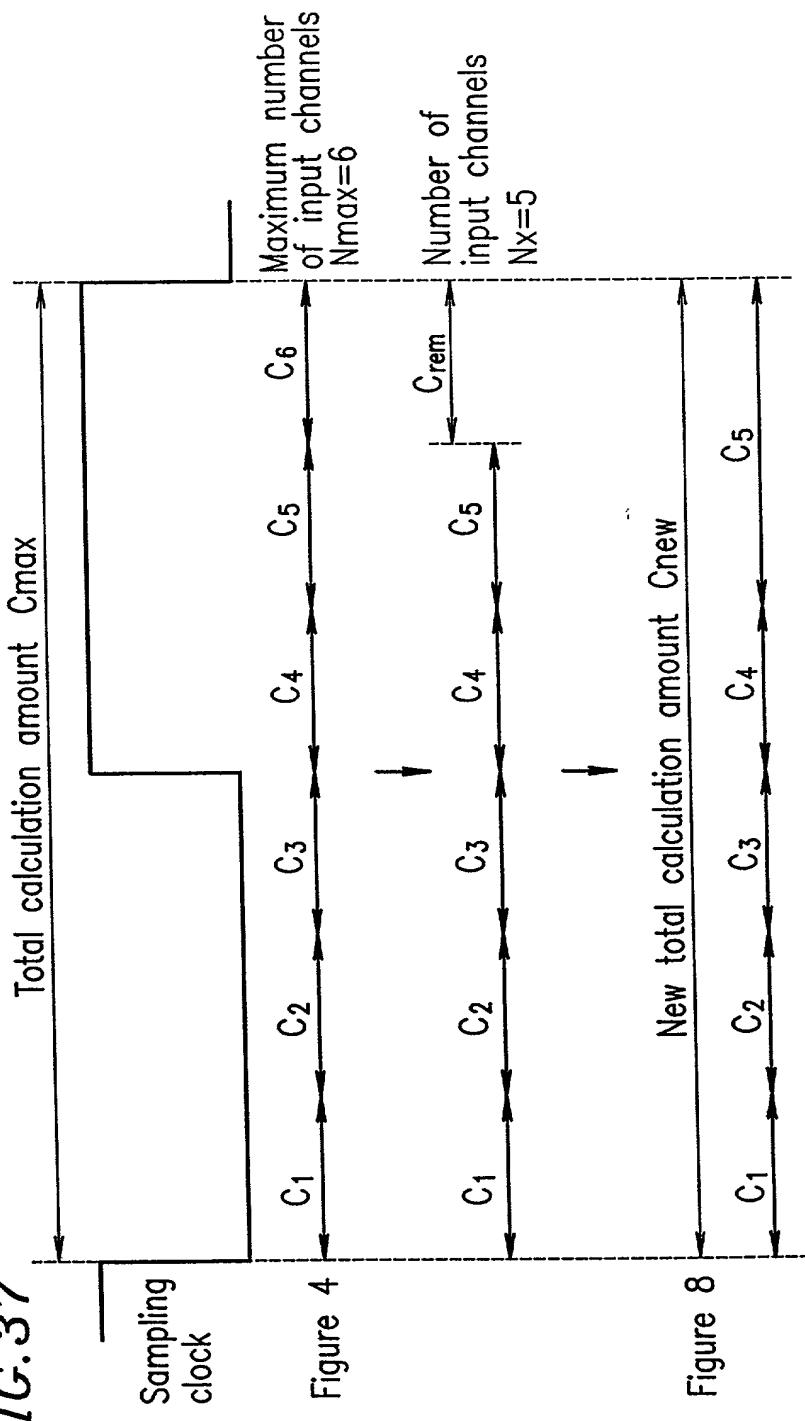
*FIG. 37*

FIG. 38

